

IDENTIFICATION

PRODUCT CODE: MAINDEC-12-D1AC-D
PRODUCT NAME: EXTENDED MEMORY CONTROL
(EXTMC12)
DATE CREATED: JUNE 19, 1970
MAINTAINER: DIAGNOSTIC GROUP
AUTHOR: HAROLD LONG

RSW = 0001 8K

8 MODE
START 20

RSW 5:1 inhibit bell
RSW 6:1

1. ABSTRACT

PDP-12 Extended Memory Control Test (Version 2) is designed to exercise all functions of memory control available to a PDP-12 with at least 4K of additional memory. This includes data field control, data handling, interrupts, data field-instruction field control during an interrupt, auto indexing in extended memory, subroutine handling (both with and without interrupts), and non-existent memory detect handling. All these tests are performed both in P mode and L mode whenever possible.

Program Control is handled by a monitor resident in bank 0. Several options are available to the user for control of error handling.

2. REQUIREMENTS

2.1 EQUIPMENT

- a) Any PDP-12 with at least 4K of extended memory.
- b) An ASR-33 teletype or equivalent

2.2 PRELIMINARY PROGRAMS

- a) All basic processor - memory tests should have been run successfully.

3. LOADING PROCEDURES

3.1 METHOD

This program must be loaded with the binary loader. If you are unfamiliar with the proper binary loading procedures refer to "Appendix A" of this program, otherwise proceed with the following:

- a) Set the teletype reader switch to FREE.
- b) Open the teletype reader and insert the program tape so that the arrows on the tape are visible to and pointing toward the operator.
- c) Close the reader and set the reader switch on START.
- d) Set the teletype front panel switch on ON LINE.
- e) Set the LEFT switches to 7777.
- f) Set the RIGHT switches to 4000.
- g) Set the MODE switch to 8 mode.
- h) Depress I/O preset.

- i) Depress START LS.
- j) When the program tape has been read the ACCUMULATOR must be 0000 if it is not, a read-in error has occurred and one might try reloading the binary loader.
- k) Remove the program tape from the reader.

4. STARTING PROCEDURES

- a) Set the right switches as outlined in section 5.1, switch settings.
- b) Set the mode switch to 8 mode.
- c) Depress I/O preset.
- d) Depress start 20.
- e) The program, when properly running, will type the contents of the pass counter at the completion of each pass.
- f) Attempting to test non-existent memory may result in false error printout or program destruction.

5. ERROR ROUTINE

5.1 SWITCH SETTINGS

- a) In general, RSW 0-6 allow selection of the error mode. With all switches equal to zero, the sequence would be: (error typeout and halt) - operator selects any additional error modes and depresses continue; machine will respond as directed by right switches.

RSW 00 = 1, SUPPRESS ERROR HALT
RSW 01 = 1, SUPPRESS ERROR PRINTOUT
RSW 02 = 1, SCOPE LOOP ON FAILING ROUTINE
RSW 03 = 1, SCOPE LOOP ON NON-FAILING ROUTINE
RSW 05 = 1, INHIBIT BELL
RSW 06 = 1, INHIBIT PASS COUNTER

- b) RSW 08-11 must contain the amount of memory available, within the range of 8 to 32K.

8K: 001
12K: 010
16K: 011
20K: 100
24K: 101
28K: 110
32K: 111

5.2 ERROR PRINTOUT

- a) The error printout has the following general form:

```
TESTNAME TEST MESSAGE FAILED  
REGISTER REGISTER REGISTER...  
(CONTENTS) (CONTENTS) (CONTENTS)...
```

The message is interpreted as follows:

TESTNAME - The mnemonic code used to identify each test in the listing.

TEST MESSAGE FAILED - What the test is attempting to check, along with the identifier "failed".

REGISTERS - The registers associated with this test; this may be the L mode data field register, the L mode save field register, etc.

(CONTENTS) - The contents of each register identified above.

Consult the listing for further explanation of any error condition encountered.

- b) Following is a list of all possible error printouts:

```
TST01  
CDF OR RDF FAILED (PMODE)  
SENT RCVD
```

```
TST02  
CDF OR RDF FAILED (PMODE)  
SENT RCVD
```

```
TST03  
LDF OR RDF FAILED (LMODE)  
SENT RCVD
```

```
TST04  
LDF OR RDF FAILED (LMODE)  
SENT RCVD
```

```
TST05  
CDF OR RDF FAILED (PMODE)  
SENT RCVD
```

```
TST06  
LDF OR RDF FAILED (LMODE)  
SENT RCVD
```

TST07
PMODE INTERRUPT FAILED

TST08
PMODE LOAD SF OR RIB FAILED
DF SF

TST9A
LMODE INTERRUPT FAILED

TST09
LMODE LOAD SF OR RIB FAILED
DF SF

TST 10
PMODE DF FAILED TO ZERO ON AN INTERRUPT
SENT SF RCVD

TST11
LMODE DF FAILED TO ZERO ON AN INTERRUPT
SENT SF RCVD

TST12
DCA I - TAD I FAILED
BANK LOCN SENT RCVD

TST13
STA - LDA FAILED
BANK LOCN SENT RCVD

TST14
LMODE JUMP SAVE RETURN FAILED FOR NORMAL JUMP

TST15
DJR FAILED TO INHIBIT JUMP SAVE

TST16
LMODE JMP FAILED TO CLEAR DJR

TST17
PMODE JUMP ALTERED CELL 0000

TST18
PMODE IOF ALTERED CELL 0000

TST19
LMODE IOF ALTERED CELL 0000

TST20
PMODE JUMP CLEARED DJR

TST21
DJR INHIBITED PMODE INTERRUPT SAVE

TST22
NON EXISTANT MEMORY READ-BACK FAILED
BANK DATA

TST24
CIF FAILED TO LOAD PROPER IF
SENT TCVD

TST25
LIF FAILED TO LOAD PROPER IF
SENT TCVD

TST26
CIF FAILED TO FIND PROPER MEMORY
SENT RCVD

TST27
PMODE INTERRUPTS NOT INHIBITED BY CIF
BANK

TST28
LMODE LIF FAILED TO INHIBIT INTERRUPTS
BANK

TST29
LMODE JMP Ø FAILED TO CLEAR INTERRUPT INHIBIT
BANK

TST30
LMODE DJR-JMP Ø FAILED TO LOAD IF
BANK

TST 32
LMODE ION-LIF FAILED TO INHIBIT INTERRUPTS

TST32
LMODE LIF-JMP N FAILED TO LOAD SF
IF DF SF

TST34
LMODE PMF IN EXTENDED BANK FAILED
BANK SF

TST35
PMODE AUTO-INDEX FAILED
BANK CELL ADDR

TST36 LMODE AUTO-INDEX FAILED
FIELD LOCN

EXT MEM TST PASS --- ØØØØ

SPURIOUS INTERPUPT
(CHECK IOC I/O PRESET)

/PDP-12 EXTENDED MEMORY TEST, VERSION 2, MAINDEC 12-D1AC-L
 /COPYRIGHT, 1970, DIGITAL EQUIPMENT CORP., MAYNARD, MASS.

/AUTHOR: HAROLD LONG

/THIS TEST IS DESIGNED TO EXERCISE ALL MEMORY
 /REFERENCE INSTRUCTIONS AVAILABLE ON A PDP-12
 /COMPUTER WITH EXTENDED MEMORY. IT OPERATES
 /IN BOTH P MODE AND L MODE, IN ALL AVAILABLE
 /MEMORY; A MINIMUM OF 8K OF CORE IS REQUIRED.

/RIGHT SWITCH REGISTER OPTIONS:

/SR00=1, INHIBIT ERROR HALT
 /SR01=1, INHIBIT ERROR PRINTOUT
 /SR02=1, SCOPE LOOP ON FAILING ROUTINE
 /SR03=1, SCOPE LOOP ON NON-FAILING ROUTINE
 /SR05=1, INHIBIT BELL
 /SR06=1, INHIBIT PASS COUNTER PRINTOUT
 /SR09,10,11 -- EXTENDED BANKS

/NORMAL SWITCH SETTING IS RSW=000N, WHERE
 /(N)=AMOUNT OF EXTENDED MEMORY AVAILABLE AND
 /IS WITHIN THE RANGE OF 0KN<10 OCTAL

/PROGRAM CONTROL IS HANDLED BY A MONITOR RESIDENT IN BANK 0, TIMES
 /LOCATIONS 5000 TO 5177. ALL ROUTINES VISIT THE MONITOR 4096 TIMES
 /AT THE COMPLETION OF A TEST, AN ERROR WILL CAUSE THE
 /PROGRAM TO TYPE OUT THE ERROR MESSAGE AND HALT. THE
 /HALT IS AT LOCATION 5033. THE HALTS IN THE PROGRAM
 /BLOCKS ARE NOT, REPEAT NOT, EXECUTED, THEY ARE
 /THERE FOR MANUAL PROGRAM CONTROL ONLY.

/TO REDEFINE AMOUNT OF MEMORY AVAILABLE, THE
 /PROGRAM MUST BE RESTARTED.

/I/O PRESET TO PAGE, START 20

```

164 /PDP-12 INSTRUCTION DEFINITIONS
165 /L MODE MEMORY REFERENCE
166 LDF=0640 /LOAD DATA FIELD 0-37
167 LIF=0600 /LOAD INSTRUCTION FIELD 2-37
168 LJR=0006 /DISABLE JUMP RETURN
169 /MODE CHANGE
170 POP=0002 /SWITCH TO P MODE
171 LINC=6141 /SWITCH TO L MODE
172 /L MODE PROGRAMMING INSTRUCTIONS
173 LUMP=6000 /JMP
174 CLR=0011
175 AZE=0450
176 ADD=2000
177 LOR=0000
178 LNOP=0016 /NOP
179 ROR=0300
180 LSKP=0456
181 ROL=0240
182 BSE=1600
183 BCL=1540
184 SET=0060
185 STC=4000
186 SRO=1500
187 LQA=1000
188 STA=1040
189 XSK=0220 /DATA MATRIX SWITCHES
190 /DATA MATRIX SWITCHES
191 EXITA=7777
192 EXITB=4444
193 EXIT=0000
194 /USED AS A SWITCH CHECK
195 /((REALLY SET I))
196 /((REALLY XSK I))
197 /SPECIAL RESTART SWITCH

```

```

/ P MODE INTERRUPT HANDLER
* 020
PINTR, 0220 /INTERPT RETURN STORAGE (ALSO LINC JUMP SAVE)
      CLA CLL CML /SET LINK, CLEAR AC
      R1B /READ SF
      DCA /SAVE IT
      TAD PPOINT /GET SWITCH
      SNA CLA /SET?
      JMP I RETURN /NO, RETURN THROUGH PRESET LINKUP
      DCA PPOINT /CLEAR SWITCH
      RNF /RESTORE MEMORY
      ISZ 0 /ENABLE RETURN
      JMP I PINTR /BACK TO MAINLINE VIA INTERRUPT RETURN LINKUP
/AUTO-INDEX REGISTERS
LREG1, 0000 /DATA POINTER
PINT, 7220 /MESSAGE POINTER
AUTO11, 0000
AUTO12, 2000
COUNT, 2220
/CROSS-PAGE REFERENCE TAGS AND CONSTANTS
* 020
      JMP 176 /MINOR START
0020 5176
0021 003
0022 007
0023 0010
0024 0017
0025 002
0026 0040
0027 0070
0030 0077
0031 0100
0032 0177
0033 0207
0034 0400
0035 1026
0036 1777
0037 2777
      K 003, 0073
      K 007, 0027
      K 010, 0010
      K 017, 0017
      K 020, 0020
      K 040, 0040
      K 070, 0070
      K 077, 0077
      K 100, 0100
      K 177, 0177
      K 207, 0207
      K 400, 0400
      K 1026, 1026
      K 1777, 1777
      K 2000, 2777

```

```

/LMODE INTERRUPT HANDLER
*040
LINTR, 0000 /INTERRUPT RETURN STORAGE
CLR /CLEAR LINK, CLEAR AC
IOB /
RIR /READ SAVE FIELD REG
STC /SAVE IT
SRO /SWITCH SET?
LPOINT
LSKP /TO HERE IF BIT 0=1
LJMP /NO, RETURN TO BANK 0 THROUGH PRESET LINKUP
LSET, 0 /
IOB /
RMF /YES, RESTORE MEMORY FIELDS
XSK /INCREMENT
ADD /GET RETURN
BSE /MAKE IT A LINK JUMP (BSE I)
6000 /
STC /STORE FOR EXECUTION
STC /CLEAR SWITCH
IOB /
ION /ENABLE INTERRUPTS
LJMP /BACK TO BANK 0 VIA INTERRUPT RETURN LINKUP
0000
LREG, 0000
LPOINT, 0000

```

```

/
/ MORE TAGS AND CONSTANTS
/
BANK, 0000
BELL, BELLS
BVKSET, LOCSET
ERROR, ERRORS
EXDF33, XDF33
EXIF33, XIF33
GETBNK, GETNXT
GETBNL, GETNXL
K5252, 5252
K6020, 6020
K7774, 7774
KCDF, CDF
KCIF, CIF
KHLT, HLT
KLDF, LDF
KIOB, IOB
KIOF, IOF
KLNOP, LNOP
KRIF, RIF
KLIF, LIF
KLJMP, LJMP
LBANK, 0003
LMASK, 0037
LSTERR, 0000
NERROR, NERRORS
PASSN, PASS
FBANK, 0000
PMASK, 0070
PNIA, LOCA
PNIB, LOCB
PNIC, LOCC
PNICA, LOCCA
PNID, LOCD
PNIE, LOCE
PNIF, LOCF
PNIG, LOCG
PNIH, LOCH
PNII, LOCI
PNIJ, LOCIJ
PNIK, LOCK
PNIL, LOCL
PNIO, LOCO
PNIP, LOCP
PNISN, PNISW
PPOINT, 0000

/AMOUNT OF EXTENDED MEMORY
/CROSS PAGE TO BELL RINGER
/BANK SET
/CROSS PAGE TO ERROR MONITOR

/CROSS PAGE TO PMODE FIND BANK
/CROSS PAGE TO LMODE FIND BANK

/PMODE CDF
/PMODE CIF
/PMODE HLT
/LMODE LDF

/LMODE NOP
/LMODE LIF
/LMODE JMP
/LINC FIELD IN USE (>4K)
/LIF/LDF MASK
/LAST ERROR POINTER
/CROSS PAGE TO NON-ERROR MONITOR

/PMODE BANK IN USE (<32K)
/CIF/CDF MASK
/INTERRUPT RETURN TEST 07
/INTERRUPT RETURN TEST 08
/INTERRUPT RETURN TEST 29
/INTERRUPT RETURN
/INTERRUPT RETURN TEST 10
/INTERRUPT RETURN TEST 11
/INTERRUPT RETURN TEST 21

```

0143	0000	PREG,	0000	/HOLDS SF
0144	5200	RANDOM,		/CROSS PAGE TO RANDOM GENERATOR
0145	0000	REGA,	0000	/DATA
0146	0000	REGB,	0000	/DATA
0147	0000	REGC,	0000	/DATA
0150	0147	REGCN,	REGC	
0151	0000	REGD,	0000	/DATA
0152	0000	REGE,	0000	/DATA
0153	5261	RELOC,		/CROSS PAGE TO RELOCATOR SUBR
0154	0000	HELPNT,	PINTR	/CROSS BANK TO INTERRUPT RETURN STORAGE
0155	0000	RETURN,	0000	/PMODE INTERRUPT RETURN IF SWITCH=0
0156	5253	SETFLG,	FLAG	/CROSS PAGE TO FLAG SET ROUTINE
0157	0000	SPACE,	0000	/DATA I/O BUFFER
0160	5400	TSTINT,	INTTST	
0161	0746	TST12N,	TST12	/CROSS PAGE TO TEST 12
0162	1004	TST13N,	TST13	/CROSS PAGE TO TEST 13
0163	1370	TST23N,	TST23	
0164	1403	TST24N,	TST24	
0165	1556	TST27N,	TST27	/CROSS PAGE TO TEST 27
0166	1613	TST28N,	TST28	
0167	1747	TST30N,	TST30	
0170	2033	TST32N,	TST32	
0171	2072	TST33N,	TST33	
0172	2152	TST34N,	TST34	
0173	2400	TST35N,	TST35	
0174	5244	TYPE,	TYP0UT	/CROSS PAGE TO TYPEOUT SUBR

```

/
/TO HERE FROM MINOR START
/
*176 SKP /DON'T RING ON STARTUP, INITIALIZE TEST
*START, JMS I BELL /GO RING BELL, RETURN TO TST01
/MAJOR START P MODE; INITIALIZATION ROUTINE
/
*200
START, LAS /READ SWITCHES
AND BANK K0007 /SAVE BANK BITS
DCA REGA /AMOUNT OF EXTENDED MEMORY
DCA LSTERR /CLEAR LOOP COUNTER
DCA PBANK /CLEAR OLD ERROR
DCA COUNT /CLEAR PASS COUNTER
TAD K0003 /SET LBANK TO UPPER MEMORY
DCA LBANK /RESTORE DATA FIELD
COF 00 /TEST FOR NO INTERRUPT
JMS I TSTINT
/PMODE
/CAN THE DATA FIELD REGISTER BE LOADED WITH ALL NUMBERS (BINARY COUNT)
/
TST01, TAD REGA /FETCH TEST NUMBER
AND PMASK /SAVE BITS 06-08
DCA REGH /SAVE FOR OBSERVATION
TAD REGH /FETCH IT
TAD KCDF /ADD CDF
+1 /PLACE IT IN ROUTINE
DCA /EXECUTE CDF N
RDE /GET DATA FIELD
AND PMASK /SAVE BITS 06-08
DCA REGC /SAVE FOR TYPING
TAD REGC /FETCH IT
DCA /2'S COMPLEMENT
TAD /COMPARE WITH DATA SENT
DCA /RESTORE DATA FIELD
DCA /INCORRECT IF NOT ZERO
JMS I /CHECK WITH MONITOR
JMS I /COF OR RDE FAILED
TST01M /MESSAGE POINTER
HLT /ERROR HALT
SKP CLA /GO TO NEXT TEST
TST01 /SCOPE LOOP, ISX LOOP

```

```

/PMODE
/CAN THE DATA FIELD BE LOADED WITH RANDOM NUMBERS
/
TST02, 4544 JMS I RANDOM /GET A RANDOM NUMBER
AND 4121 PMASK /SAVE BITS 06-08
DCA 3146 REGB /SAVE FOR OBSERVATION
TAD 1146 REGR /FETCH IT
TAD 1141 KCDF /ADD CDF
DCA 3246 .+1 /PLACE IT IN ROUTINE
W000 /EXECUTE CDF R
RDF 6214 /GET DATA FIELD
AND 1121 PMASK /SAVE BITS 06-08
DCA 3147 REGC /SAVE FOR TYPING
TAD 1147 REGR /FETCH IT
CIA 7041 /2'S COMPLEMENT
TAD REGB /COMPARE
CDF 1146 /RESTORE DATA FIELD
SNA CLA 6201 /INCORRECT IF NOT ZERO
JMS I 4516 /CHECK WITH MONITOR
JMS I 4471 /CDF FAILED
TST02M /MESSAGE POINTER
HLT 7412 /ERROR HLT
SKP CLA /NEXT TEST
TST02 /SCOPE LOOP; ISZ LOOP

```

```

/LMODE
/CAN THE DATA FIELD REGISTER BE LOADED WITH BINARY COUNT
/
TST03, 1145 REGA /FETCH TEST NUMBER
AND 1114 LMASK /SAVE BITS 07-11
DCA 3146 REGB /SAVE FOR OBSERVATION
TAD 1146 REGR /FETCH IT
TAD 1141 KCDF /ADD CDF
DCA 3274 .+2 /PLACE IN ROUTINE
LING /GO TO LING MODE
W000 /EXECUTE LDF
ICR 6214 /PREPARE TO GET DATA FIELD
RDF 711 /GET DATA FIELD
RGR CLL /BACK TO PMODE
DCA 3147 REGC /JUSTIFY RIGHT TO AGREE WITH REGR
TAD 1147 REGR /SAVE FOR TYPING
CIA 7041 /FETCH IT
TAD REGB /2'S COMPLEMENT
CDF 1146 /COMPARE
SNA CLA 6201 /RESTORE DATA FIELD
JMS I 4516 /INCORRECT IF NOT ZERO
JMS I 4471 /CHECK WITH MONITOR
TST03M /CDF FAILED
HLT 7412 /MESSAGE POINTER
SKP CLA /ERROR HLT
TST03 /GO TO NEXT TEST
/SCOPE LOOP; ISZ LOOP

```



```

0315 4544 /LMODE
0316 1114 /CAN THE DATA FIELD REGISTER BE LOADED WITH RANDOM NUMBERS
0317 3146 /
0318 1146 TST04, JMS I RANDCV
0319 1146 AND LMASK
0320 1146 DCA REG8
0321 1146 TAD REG8
0322 3324 TAD KLDF
0323 6141 .+2
0324 8000 DCA LINC
0325 6500 0020
0326 6214 IOR
0327 8002 RDF
0330 7110 PDP CLL
0331 3147 DCA REGC
0332 1147 TAD REGC
0333 7041 CIA
0334 1146 TAD REG8
0335 6201 CDF
0336 765 SNA CLA
0337 4516 JMS I ERROR
0340 4471 JMS I ERROR
0341 5613 TST04M
0342 7402 HLT
0343 761 SKP CLA
0344 0315 TST04

```

```

/GET RANDOM NUMBER
/SAVE BITS 07-11
/SAVE FOR OBSERVATION
/FEICH IT
/ADD LF
/PLACE IN ROUTINE
/GO TO LINC MODE
/EXECUTE LDF
/APREPARE TO GET DATA FIELD
/GET DATA FIELD
/BACK TO PMODE
/JUSTIFY RIGHT TO AGREE WITH REG8
/SAVE FOR TYPING
/FEICH IT
/215 COMPLEMENT
/COMPARE
/RESTORE DATA FIELD
/INCORRECT IF NOT ZERO
/CHECK WITH MONITOR
/LDF FAILED
/MESSAGE POINTER
/ERROR HALT
/GO TO NEXT TEST
/SCOPE LOOP; ISZ LOOP

```

```

/PMODE
/GATE SHAKER TEST
/
TST05, JMS I RANDOM
AND PMASK
DCA REG8
TAD REG8
TAD KCOF
DCA NOW1
CDF 40
CDF 20
CDF 10
CDF 70
CDF 60
CDF 50
CDF 40
CDF 30
CDF 20
CDF 10
CDF 20
CDF 30
CDF 40
CDF 50
CDF 60
CDF 70
NOW1, 0000
RDF 0000
AND
DCA
TAD
CIA
TAD
CDF
SNA CLA
JMS I NERROR
JMS I ERROR
TST05M
HLT
SKP CLA
TST05
3345 4544
3346 1121
3347 3146
3350 1146
3351 1101
3352 3374
3353 6201
3354 6241
3355 6221
3356 6211
3357 6271
3360 6261
3361 6251
3362 6241
3363 6231
3364 6221
3365 6211
3366 6221
3367 6231
3370 6241
3371 6251
3372 6261
3373 6271
3374 0000
3375 6214
3376 1121
3377 3147
3400 1147
3401 7041
3402 1146
3403 6201
3404 7651
3405 4516
3406 4471
3407 5645
3410 7412
3411 7611
3412 1112
/EXECUTE ACTUAL CDF
/GET DATA FIELD
/SAVE BITS 06-08
/SAVE FOR TYPING
/FETCH IT
/2'S COMPLEMENT
/COMPARE
/RESTORE DATA FIELD
/INCORRECT IF NOT ZERO
/CHECK WITH MONITOR
/PROBLEMS WITH NOISY DATA FIELD
/MESSAGE POINTER
/ERROR HALT
/GO TO NEXT TEST
/SCOPE LOOP; ISZ LOOP

```

```

/ LMODE
/ GATE SHAKER TEST
/
TST06, JMS I RANDOM
AND LMASK
DCA REG8
TAD REG8
TAD KLDF
DCA NOW2
LINC
LDF 00
LDF 37
LDF 20
LDF 10
LDF 04
LDF 02
LDF 01
LDF 25
LDF 12
LDF 07
LDF 30
LDF 00
LDF 01
LDF 02
LDF 03
LDF 04
LDF 05
LDF 06
LDF 06
LDF 07
LDF 10
LDF 17
LDF 27
LDF 37
NOW2, 0000
IOB
RDF
POP CLL
DCA REGC
TAD REGC
CIA
TAD REG8
CDF
SNA CLA
JMS I ERROR
JMS I ERROR
TST06M
HLT
SKP CLA
TST06
0413 4544
0414 0114
0415 3146
0416 1146
0417 1104
0420 3252
0421 6141
0422 0641
0423 0677
0424 0660
0425 0650
0426 0644
0427 0642
0430 0641
0431 0665
0432 0652
0433 0647
0434 0670
0435 0641
0436 0641
0437 0642
0440 0643
0441 0644
0442 0645
0443 0646
0444 0646
0445 0647
0446 0650
0447 0657
0450 0667
0451 0677
0452 0000
0453 0500
0454 6214
0455 0302
0456 7110
0457 3147
0460 1147
0461 7041
0462 1146
0463 6201
0464 765
0465 4516
0466 4471
0467 5677
0470 7402
0471 7610
0472 0413
/GET A RANDOM NUMBER
/SAVE BITS 07-11
/SAVE FOR OBSERVATION
/FETCH IT
/ADD LDF
/STORE FOR EXECUTION
/GO TO LINC MODE
/TRY SOME DATA FIELD
/NOISEMAKERS

/EXECUTE ACTUAL LDF
/PREPARE TO GET DATA FIELD
/GET DATA FIELD
/GO TO PMODE
/JUSTIFY WITH REG8
/SAVE FOR TYPING
/FETCH IT
/2'S COMPLEMENT
/COMPARE
/RESTORE DATA FIELD
/INCORRECT IF NOT ZERO
/CHECK WITH MONITOR
/PROBLEMS WITH NOISY DATA FIELD
/MESSAGE POINTER
/ERROR HALT
/GO TO NEXT TEST
/SCOPE LOOP; IS2 LOOP

```

/THE DATA FIELD IS NOW CONSIDERED TO BE TESTED,

/NOW CHECK RIB

/PMODE

/CHECK INTERRUPT FACILITY.

```

0473 6041      /CHECK FOR FLAG
0474 4556      /NOT UP; GO SET IT
0475 1122      /GET ADDRESS RETURN
0476 3155      /STORE IT
0477 3142      /ZERO THE PMODE SWITCH
0500 6001      /ENABLE INTERRUPT
0501 7000      /WAIT
0502 6002      /DISABLE INTERRUPT
0503 7430      /CHECK LINK; INCORRECT IF ZERO
0504 4516      /CHECK WITH MONITOR
0505 4471      /INTERRUPT FAILED
0506 5731      /MESSAGE POINTER
0507 7402      /ERROR HALT
0510 7610      /GO TO NEXT TEST
0511 0473      /SCOPE LOOP; ISZ LOOP

```

/PMODE

/NOW CHECK RIB

```

0512 6041      /CHECK FOR FLAG
0513 4556      /NOT UP; GO SET IT
0514 1123      /GET RETURN ADDRESS
0515 3155      /STORE IT
0516 4544      /GET RANDOM NUMBER
0517 0121      /SAVE BITS 06-08
0520 3146      /SAVE FOR OBSERVATION
0521 1146      /FETCH IT
0522 1101      /ADD CDF
0523 3324      /STORE FOR EXECUTION
0524 1000      /EXECUTE CDF
0525 6001      /ENABLE INTERRUPT
0526 7000      /WAIT
0527 6002      /DISABLE INTERRUPT
0530 6234      /READ INTERRUPT BUFFER
0531 7006      /JUSTIFY WITH REG8
0532 7104      /SOME MORE
0533 0121      /SAVE BITS 06-08
0534 3147      /SAVE FOR TYPING
0535 1147      /FETCH IT
0536 7041      /2'S COMPLEMENT
0537 1146      /COMPARE
0540 6201      /RESTORE DATA FIELD
0541 7651      /INCORRECT IF NOT ZERO
0542 4516      /CHECK WITH MONITOR
0543 4471      /LOAD SF OR RIB FAILED
0544 5752      /MESSAGE POINTER
0545 7402      /ERROR HALT
0546 7610      /GO TO NEXT TEST
0512 0512      /SCOPE LOOP; ISZ LOOP

```

```

0550 6041 /LMODE
0551 4556 /CHECK INTERRUPT FACILITY
0552 1125
0553 0036
0554 1112
0555 3050
0556 3065
0557 7120
0562 6141
0561 0501
0562 6001
0563 0016
0564 0500
0565 6002
0566 0002
0567 7420
0570 4516
0571 4471
0572 6004
0573 7402
0574 7610
0575 55

TST9A, TSF JMS I SETFLG
TAD PNICA
AND K1777
TAD KLJMP
DCA LSET
DCA LPOINT
CLL CML
LINC
IOB
ION
LNOP
IOB
IOF
PDP
SNL
JMS I NERROR
JMS I ERROR
TST9AM
HLT
SKP CLA
TST9A

LOCCA,
/NOT UP: GO SET IT
/GET RETURN ADDRESS
/10 BIT ADDRESS
/ADD LINC JUMP
/STORE FOR EXECUTION
/ZERO THE LMODE SWITCH
/SET LINK
/GO TO LINC MODE
/PREPARE TO EXECUTE IOT
/ENABLE INTERRUPTS
/WAIT
/PREPARE TO EXECUTE IOT
/DISABLE INTERRUPTS
/BACK TO PMODE
/CHECK LINK, INCORRECT IF SET
/CHECK WITH MONITOR
/INTERRUPT FAILED
/MESSAGE POINTER
/ERROR HALT
/GO TO NEXT TEST
/ISZ LOOP: SCOPE LOOP

```

/LMODE

/CHECK RIB

/TST09,

0576	6041	JMS I	SETFLG	/CHECK FOR FLAG
0577	4556	TAD	PNTC	/NOT UP! GO SET IT
0600	1124	AND	K1777	/GET RETURN ADDRESS
0601	0036	TAD	KLJMP	/10 BIT ADDRESS
0602	1112	DCA	LSET	/ADD LINC JUMP
0603	3050	JMS I	RANDOM	/STORE IN RETURN ADDRESS
0604	4544	AND	LMASK	/GET RANDOM NUMBER
0605	0114	DCA	REG8	/SAVE BITS 07-11
0606	3146	TAD	REG8	/SAVE FOR COMPARISON
0607	1146	TAD	KLDF	/FETCH IT
0610	1104	DCA	CLL CML	/ADD LDF
0611	3214	LINC	0000	/STORE FOR EXECUTION
0612	7120	IOB	108	/SET LINK
0613	6141	ION	LNCP	/GO TO LINC MODE
0614	0001	IOB	103	/EXECUTE LDF
0615	0520	IOB	103	/PREPARE FOR IOT
0616	6001	IOB	103	/ENABLE INTERRUPT
0617	0016	IOB	103	/WAIT
0620	0500	IOB	103	/PREPARE FOR IOT
0621	6002	IOB	103	/DISABLE INTERRUPT
0622	0500	IOB	103	/PREPARE FOR IOT
0623	6234	IOB	103	/READ INTERRUPT BUFFER
0624	0242	ROL	2	/JUSTIFY WITH REG8
0625	0002	POP	LMASK	/BACK TO PMODE
0626	0114	AND	REGC	/SAVE BITS 07-11
0627	3147	DCA	REGC	/SAVE FOR TYPING
0630	1147	TAD	REGC	/FETCH IT
0631	7041	CIA	REG8	/2'S COMPLEMENT
0632	1146	TAD	00	/COMPARE
0633	6201	COF	00	/RESTORE DATA FIELD
0634	7657	SNA CLA	00	/INCORRECT IF NOT ZERO
0635	4516	JMS I	ERROR	/CHECK WITH MONITOR
0636	4471	JMS I	ERROR	/LMODE RIB FAILED
0637	6025	TST09M	00	/MESSAGE POINTER
0640	7402	HLT	00	/ERROR HALT
0641	7610	SKP CLA	00	/GO TO NEXT TEST
0642	1576	TST09	00	/SCOPE LOOP: ISZ LOOP

```

0643 0041 /PMODE
0644 4556 /DOES THE DATA FIELD SET TO ZERO FOR AN INTERRUPT
0645 1126 /
0646 3155 TST10, ISF
0647 4544 JMS I SETFLG
0650 1121 TAD PNTD
0651 3146 DCA RETURN1
0652 1146 JMS I RANDOM
0653 1101 AND PMASK
0654 3255 DCA REGB
0655 0000 TAD REGB
0656 6001 DCA KODF
0657 7000 ,+1
0660 6002 ION
0661 6234 NOP
0662 7006 IOF
0663 7004 RIR
0664 3147 RAL
0665 6214 DCA
0666 0121 RDF
0667 3151 AND
0670 1151 DCA
0671 6211 TAD
0672 7650 CDF
0673 4516 SNA CLA
0674 4471 JMS I NERROR
0675 6057 JMS I ERROR
0676 7402 TST10M
0677 7611 HLT
0700 0643 SKP CLA
TST10,
LOC0,
/NO UP) GO SET IT
/GET RETURN ADDRESS
/STORE IT
/GET RANDOM NUMBER
/SAVE BITS 06-08
/SAVE FOR TYPING
/FETCH IT
/ADD CDF
/STORE FOR EXECUTION
/EXECUTE CDF
/ENABLE INTERRUPT
/WAIT
/DISABLE INTERRUPT
/GET INTERRUPT BUFFER
/JUSTIFY WITH REGB
/SOME MORE
/SAVE FOR TYPING
/READ DATA FIELD
/SAVE BITS 06-08
/STORE FOR TYPING
/FETCH IT
/RESTORE DATA FIELD
/INCORRECT IF NOT ZERO
/CHECK WITH MONITOR
/MESSAGE POINTER
/GO TO NEXT TEST
/SCOPE LOOP: IS? LOOP

```

```

/PMODE
/DOES THE DATA FIELD SET TO ZERO FOR AN INTERRUPT
TST11, TSF
JMS I SETFLG
TAD PNIE
AND K1777
TAD KLJMP
DCA LSET
JMS I RANDOM
AND LMASK
DCA REG8
TAD REG8
TAD KLDF
DCA L+2
LINC
0000
IOB
ION
LNOP
IOB
IOF
IOB
R18
ROL
STC
IOB
RDF
PDP
RAR CLL
DCA
TAD
CDF
SNA CLA
JMS I NERROR
JMS I ERROR
TST11M
HLT
SKP CLA
TST11
LOCE,
0701 6041
0702 4556
0703 1127
0704 0036
0705 1112
0706 3057
0707 4544
0710 0114
0711 3146
0712 1146
0713 1104
0714 3316
0715 6141
0716 0000
0717 0500
0720 6001
0721 0016
0722 0500
0723 6002
0724 0500
0725 6234
0726 0242
0727 4147
0730 0500
0731 6214
0732 0002
0733 7117
0734 3151
0735 1151
0736 6201
0737 7657
0740 4516
0741 4471
0742 6123
0743 7412
0744 7617
0745 1701
/NOT UP; GO SET IT
/GET RETURN ADDRESS
/10 BIT ADDRESS
/ADD LINC MODE JMP
/STORE IT
/GET RANDOM NUMBER
/SAVE BITS 07-11
/STORE FOR TYPING
/FETCH IT
/ADD LDF
/STORE FOR EXECUTION
/GO TO LINC MODE
/EXECUTE LDF
/PREPARE FOR IOT
/ENABLE INTERRUPT
/WAIT
/PREPARE FOR IOT
/DISABLE INTERRUPT
/PREPARE FOR IOT
/READ INTERRUPT BUFFER
/JUSTIFY WITH REG8
/SAVE FOR TYPING
/PREPARE FOR IOT
/READ DATA FIELD
/BACK TO PMODE
/JUSTIFY WITH REG8
/SAVE FOR TYPING
/FETCH IT
/RESTORE DATA FIELD
/INCORRECT IF NOT ZERO
/CHECK WITH MONITOR
/DATA FIELD FAILED TO ZERO ON INTERRUPT
/MESSAGE POINTER
/ERROR HALT
/GO TO NEXT TEST
/SCOPE LOOP; ISZ LOOP

```



```

2746 7300
2747 4474
0750 7450
0751 5562
0752 3146
0753 1025
0754 3145
0755 1146
0756 7006
0757 7004
0760 1101
0761 3363
0762 1076
0763 0000
0764 3545
0765 1545
0766 6201
0767 3147
0770 1147
0771 7041
0772 1076
0773 6201
0774 7650
0775 4516
0776 4471
0777 6167
1000 7402
1001 7610
1002 7755
1003 5561

/PMODE
/DOES DCA I--TAD I WORK FOR ALL DATA FIELDS
/
TST12: CLA CLL
      JMS I GETBNK
      SNA I
      JMP I TST13N
      DCA REGC
      TAD K0020
      DCA REGA
      TAD REGC
      TST12A, RTL
      RAL
      TAD
      DCA EXC12
      TAD K5252
      EXC12, 0000
      DCA I REGA
      TAD I REGA
      CDF 00
      DCA REGC
      TAD REGC
      CIA
      TAD K5252
      CDF 00
      SNA CLA
      JMS I NERROR
      JMS I ERROR
      TST12M
      HLT
      SKP CLA
      TST12A
      JMP I TST12N

      /CLEAR AC
      /GET NEXT BANK
      /DONE?
      /YES, NEXT TEST VIA PAGE 0
      /SAVE BANK
      /GET CONSTANT
      /SET REGA = 20
      /GET CURRENT BANK
      /JUSTIFY
      /JUSTIFY
      /GET CDF
      /STORE FOR EXECUTION
      /GET CONSTANT
      /EXECUTE CDF
      /STORE IN TEST BANK
      /GET IT
      /RESTORE DATA FIELD
      /SAVE DATA
      /FETCH IT
      /2'S COMPLEMENT
      /COMPARE
      /RESTORE DATA FIELD
      /INCORRECT IF NOT ZERO
      /CHECK WITH MONITOR
      /DCA I OR TAD I FAILED
      /MESSAGE POINTER
      /ERROR HALT
      /TO NEXT BANK
      /SCOPE LOOP; ISZ LOOP
      /NEXT BANK VIA PAGE 0

```

```

1004 7300 /LMODE
1005 4475 /DOES STA-LDA WORK FOR ALL DATA FIELDS
1006 7450 /
1007 5244 TST13, CLA CLL
1010 3146 JMS I GETBNL
1011 1077 SNA
1012 3145 JMP
1013 1146 DCA
1014 1104 TAD
1015 3222 DCA
1016 1145 TAD
1017 3013 DCA
1020 1076 TAD
1021 6141 LINC
1022 0000 TST13A,
1023 1053 EXC13,
1024 1013 STA
1025 0640 LDA
1026 0002 LDF
1027 3147 PDP
1030 1147 DCA
1031 7041 TAD
1032 1076 CIA
1033 6201 TAD
1034 765 CDF
1035 4516 SNA CLA
1036 4471 JMS I
1037 6225 JMS I
1040 7402 TST13M
1041 761 HLT
1042 1016 SKP CLA
1043 5214 TST13A
JMP TST13

```

/CLEAR AC
 /FIND NEXT BANK
 /DONE
 /YES, GO TO NEXT TEST
 /SAVE BANK
 /GET CONSTANT
 /SET REGA TO 6020
 /GET CURRENT BANK
 /ADD LDF
 /STORE FOR EXECUTION
 /GET ADDRESS
 /STORE FOR INDIRECT ACCESS
 /GET CONSTANT
 /GO TO LMODE
 /EXECUTE LDF
 /STORE INDIRECT TO DF
 /FETCH NUMBER
 /RESTORE DATA FIELD
 /TO PMODE
 /SAVE FOR TYPING
 /FETCH IT
 /2'S COMPLEMENT
 /COMPARE
 /RESTORE DATA FIELD
 /INCORRECT IF NOT ZERO
 /CHECK WITH MONITOR
 /STA OR LDA FAILED
 /MESSAGE POINTER
 /ERROR HALT
 /NEXT TEST
 /SCOPE LOOP: ISZ LOOP
 /NEXT BANK

/
 /TEST THE DJR FUNCTION FOR ALL COMBINATIONS

/LMODE

/DOES DJR NOT FUNCTION WHEN NOT SET?

1044	7300	TST14,	CLA CLL	/CLEAR AC
1045	1076	TAD	K5252	/GET CONSTANT
1046	3000	DCA	0	/SET 0
1047	6141	LINC		/GO TO LINC MODE
1050	7051	LJMP	+1	/DO A LINC JUMP
1051	0002	POP		/BACK TO P MODE
1052	1000	TAD	0	/GET 0
1053	7041	CIA		/2'S COMPLEMENT
1054	1076	TAD	K5252	/ADD CONSTANT
1055	7640	SZA CLA		/WAS LOCATION 0 CHANGED?
1056	4516	JMS I	NERROR	/YES: CHECK WITH MONITOR
1057	4471	JMS I	ERROR	/LINC JUMP SAVE RETURN FAILED
1060	6261	TST14M		/MESSAGE POINTER
1061	7402	HLT		/ERROR HALT
1062	7610	SKP CLA		/TO NEXT TEST
1063	1004	TST14		/SCOPE LOOP; ISZ LOOP

/LMODE

/DOES DJR FUNCTION WHEN IT'S SET?

1064	7300	TST15,	CLA CLL	/CLEAR AC
1065	1076	TAD	K5252	/GET CONSTANT
1066	3000	DCA	0	/SET 0
1067	6141	LINC		/TO L MODE
1070	0006	DJR		/DISABLE JUMP SAVE RETURN
1071	7072	LJMP	+1	/DO A LINC JUMP
1072	0002	PDP		/BACK TO PMODE
1073	1000	TAD	0	/GET 0
1074	7041	CIA		/2'S COMPLEMENT
1075	1076	TAD	K5252	/COMPARE WITH CONSTANT
1076	7650	SNA CLA		/DID DJR WORK?
1077	4516	JMS I	NERROR	/CHECK WITH MONITOR
1100	4471	JMS I	ERROR	/DJR FAILED
1101	6316	TST15M		/MESSAGE POINTER
1102	7402	HLT		/ERROR HALT
1103	7610	SKP CLA		/TO NEXT TEST
1104	1004	TST15		/SCOPE LOOP; ISZ LOOP

```
1105 7302 /LMODE
1106 1076 /DOES A LINC JUMP CLEAR DJR?
1107 3000 /
1110 6141 TST16, CLA CLL
1111 0026 TAD K5252
1112 7113 DCA 0
1113 7114 LINC
1114 0002 DJR
1115 1000 JMP ,+1
1116 7041 LUMP ,+1
1117 1076 PDP
1118 1076 TAD 0
1119 7041 CIA
1120 7640 TAD K5252
1121 4516 SZA CLA
1122 4471 JMS I NERROR
1123 6344 JMS I ERROR
1124 7402 TST16M
1125 7610 HLT
1126 1105 SKP CLA
TST16
/PMODE
/DOES JUMP SAVE RETURN WORK FOR 8 MODE JUMPS?
1127 7302 /
1128 1076 TST17, CLA CLL
1129 3000 TAD K5252
1130 5333 DCA 0
1131 1000 JMP ,+1
1132 7041 TAD 0
1133 1076 CIA
1134 7650 TAD K5252
1135 4516 SNA CLA
1136 4471 JMS I NERROR
1137 4471 JMS I ERROR
1140 6371 TST17M
1141 7402 HLT
1142 7610 SKP CLA
1143 1127 TST17
1144 1127 /CLEAR AC
/GET CONSTANT
/SET 0
/TO LMODE
/DISABLE JUMP SAVE RETURN
/DO A LINC JUMP
/DO ANOTHER LINC JUMP
/BACK TO PMODE
/GET 0
/2'S COMPLEMENT
/COMPARE WITH CONSTANT
/DID DJR CLEAR?
/CHECK MONITOR
/DJR FAILED TO CLEAR
/MESSAGE POINTER
/ERROR HALT
/TO NEXT TEST
/SCOPE LOOP; ISZ LOOP
/CLEAR AC
/GET CONSTANT
/SET 0
/DO AN 8 MODE JUMP
/GET 0
/2'S COMPLEMENT
/COMPARE WITH CONSTANT
/DID WE SAVE IN ERROR?
/CHECK MONITOR
/JUMP SAVE RETURN OPERATED IN ERROR
/MESSAGE POINTER
/ERROR HALT
/TO NEXT TEST
/ISZ LOOP; SCOPE LOOP
```

```

1145 7300 /PMODE
1146 1076 /DOES JUMP SAVE RETURN WORK FOR NON-JUMP COMMANDS?
1147 3000 /
1150 6002 TST18, CLA CLL /CLEAR AC
1151 1000 TAD TAD K5252 /GET CONSTANT
1152 7041 DCA 0 /SET 0
1153 1076 IOF 0 /IOF LOOKS LIKE LINC JUMP
1154 7650 TAD 0 /GET 0
1155 4516 CIA K5252 /2'S COMPLEMENT
1156 4471 SNA CLA /COMPARE WITH CONSTANT
1157 6415 JMS I NERROR /DID CELL 0 CHANGE?
1160 7402 JMS I ERROR /CHECK MONITOR
1161 7612 TST18M /IOF CHANGED CELL 0
1162 1145 HLT 7402 /MESSAGE POINTER
1163 1163 SKP CLA /ERROR HALT
1164 1076 /TO NEXT TEST
1165 3000 /SCOPE LOOP; ISZ LOOP
1166 6141 /
1167 0500 /LMODE
1170 6002 /DOES JUMP SAVE RETURN WORK FOR NON-JUMP COMMANDS?
1171 2002 /
1172 1000 TST19, CLA CLL /CLEAR AC
1173 7041 TAD TAD K5252 /GET CONSTANT
1174 1076 DCA 0 /SET 0
1175 765 LINC /GO TO LMODE
1176 4516 IOB /PREPARE FOR IOT
1177 4471 IOF /DISABLE INTERRUPTS
1178 6441 PDP /BACK TO PMODE
1179 7402 TAD 0 /FETCH 0
1180 1000 CIA K5252 /2'S COMPLEMENT
1181 1076 TAD TAD /ADD CONSTANT
1182 7612 SNA CLA /EQUAL?
1183 1163 JMS I NERROR /CHECK MONITOR
1184 1076 JMS I ERROR /IOB/IOF CAUSED LOC 0000 TO ALTER
1185 3000 TST19M /MESSAGE POINTER
1186 6141 HLT 7402 /ERROR HALT
1187 0500 SKP CLA /TO NEXT TEST
1188 1000 TST19 /ISZ LOOP; SCOPE LOOP
1189 7402 1163
1190 1163

```

```

1204 7300 /LMODE
1205 1076 /DOES DJR CLEAR WITH 8 MODE JUMP?
1206 300 /
1207 6141 TST20, CLA CLL /CLEAR AC
1210 0006 TAD K5252 /GET CONSTANT
1211 0002 DCA 0 /SET 0
1212 5213 DJR LINC /TO LMODE
1213 6141 DJR PDP /DISABLE JUMP RETURN SAVE
1214 7215 JMP ,+1 /TO PMODE
1215 0002 LINC ,+1 /JUMP
1216 1000 LJP ,+1 /TO LMODE
1217 7041 TAD 0 /JUMP
1220 1076 CIA /FETCH 0
1221 7650 TAD K5252 /2'S COMPLEMENT
1222 4516 SNA CLA /ADD CONSTANT
1223 4471 JMS I NERROR /EQUAL?
1224 6465 JMS I ERROR /CHECK MONITOR
1225 7402 TST20M /8 MODE JUMP CLEARED DJR
1226 7610 HLT /MESSAGE POINTER
1227 1204 SKP CLA /ERROR HALT
      TST20 /TO NEXT TEST
      /ISZ LOOP; SCOPE LOOP

```

```

1230 730
1231 1130
1232 3155
1233 1076
1234 3000
1235 6041
1236 4556
1237 6141
1238 0006
1239 0002
1240 6001
1241 7000
1242 6002
1243 7000
1244 6002
1245 7000
1246 1000
1247 7041
1248 1076
1249 7647
1250 4516
1251 4471
1252 6536
1253 7432
1254 7617
1255 1237
1256 7347
1257 3145

/PMODE
/DOES DJR INHIBIT 8 MODE INTERRUPT SAVE?
/
TST21, CLA CLL
TAD PNTF
OCA RETURN
TAD K5252
OCA 0
TSF SETFLG
JMS I SETFLG
LINC
DJR
PDP
ION
NOP
IOF
SKP 0
TAD 0
CIA K5252
TAD
SZA CLA
JMS I NERROR
JMS I ERROR
TST21M
HLT
SKP CLA
TST21
CLA CLL CMA
OCA REGA

LOCF,
/GET RETURN POINTER TO LOCF
/SET UP INTERRUPT HANDLER
/GET CONSTANT
/STORE IN 0
/FLAG SET?
/NO, GO SET IT
/TO LMODE
/SET DJR
/TO PMODE
/ENABLE INTERRUPTS
/WAIT
/DISABLE INTERRUPTS
/IF NO INTERRUPT, THIS CAUSES ERROR
/GET 0
/2'S COMPLEMENT
/ADD CONSTANT
/EQUAL?
/CHECK MONITOR
/DJR INHIBITED 8 MODE INTERRUPT
/MESSAGE POINTER
/ERROR HALT
/TO NEXT TEST
/ISZ LOOPJ SCOPE LOOP
/SET AC=7777
/PRESET REGA FOR NEXT TEST

```

```

1262 7300 /PMODE
1263 1066 /WILL NON-EXISTANT MEMORY DETECT WORK FOR ALL BANKS?
1264 3151 /
1265 1151 /TST22, CLA CLL
1266 7041 TAD BANK
1267 1022 DCA REGD
1268 3146 TAD REGD
1269 1146 TAD K0007
1270 7450 DCA REGB
1271 5370 TAD REGB
1272 7010 SNA TST23
1273 7620 JMP
1274 5342 RAR CLA
1275 5316 SNL CLA
1276 7300 JMP READ1
1277 6201 JMP READ0
1278 1151 CDF 00
1279 7041 TAD REGD
1280 1022 CIA K0007
1281 7640 TAD SZA CLA
1282 5342 JMP READ1
1283 4516 JMS I NERROR
1284 4471 JMS I ERROR
1285 6535 TST22M
1286 7402 HLT
1287 7410 SKP
1288 1262 TST22
1289 5370 JMP TST23

```



```
1316 7300 /PMODE
1317 2151 /READ 0 ROUTINE FOR TST22 (USED ONLY ONCE PER PASS)
1320 1151 /
1321 7006 READ0: CLA CLL
1322 7004 ISZ REGD
1323 1101 RTL REGD
1324 3325 RAL
1325 0000 KCDF
1326 7043 ,+1
1327 3015 DCA AUTO11
1328 3000 0
1329 1415 TAD I AUTO11
1330 5337 SZA
1331 7440 JMP ,+4
1332 5337 ISZ 0
1333 2000 JMP ,+4
1334 5331 JMP BAK22
1335 5303 CDF 00
1336 6201 DCA REGC
1337 3147 JMP FAL22
1340 5310
1341
```

```
1316 7300 /CLEAR AC
1317 2151 /INCREMENT NON-EXIST BANK
1320 1151 /FETCH BANK NUMBER
1321 7006 /JUSTIFY
1322 7004 /JUSTIFY
1323 1101 /FETCH CDF 00
1324 3325 /STORE FOR EXECUTION
1325 0000 /EXECUTE CDF
1326 7043 /SET AC=7777
1327 3015 /SETUP POINTER REGISTER
1328 3000 /SETUP COUNTER
1329 1415 /FETCH NON-EXISTANT WORD
1330 5337 /ZERO?
1331 7440 /NO, ENTER ERROR ROUTINE
1332 5337 /INCREMENT COUNTER
1333 2000 /TRY NEXT LOCATION
1334 5331 /BANK FINISHED; RETURN
1335 5303 /RESTORE DATA FIELD
1336 6201 /SAVE AC FOR TYPEOUT
1337 3147 /TO ERROR MONITOR
1340 5310
```

```
1342 7300 /PMODE
1343 2151 /READ 1 ROUTINE FOR TEST 22
1344 1151 /
1345 7006 READ1: CLA CLL
1346 7004 ISZ REGD
1347 1101 RTL REGD
1348 3351 RAL
1349 3351 KCDF
1350 3351 ,+1
1351 2000 DCA AUTO11
1352 7043 0
1353 3015 TAD I AUTO11
1354 3000 0
1355 1415 TAD I AUTO11
1356 7043 CMA
1357 7440 SZA
1358 5364 JMP ,+4
1359 2000 ISZ ,+5
1360 5355 JMP BAK22
1361 5303 JMP
1362 5303 CMA
1363 7043 DCA REGC
1364 3147 DCA 00
1365 6201 CDF FAL22
1366 5310 JMP
```

```
1342 7300 /CLEAR AC
1343 2151 /INCREMENT NON-EXIST BANK
1344 1151 /FETCH IT
1345 7006 /JUSTIFY
1346 7004 /JUSTIFY
1347 1101 /FETCH CDF 00
1348 3351 /STORE FOR EXECUTION
1349 3351 /EXECUTE CDF
1350 3351 /SET AC=7777
1351 2000 /SETUP POINTER REGISTER
1352 7043 /SETUP COUNTER
1353 3015 /FETCH NON-EXISTANT WORD
1354 3000 /COMPLEMENT
1355 1415 /ZERO?
1356 7043 /NO, ENTER ERROR ROUTINE
1357 7440 /INCREMENT COUNTER
1358 5364 /TRY NEXT LOCATION
1359 2000 /BANK FINISHED; RETURN;
1360 5355 /RESTORE AC
1361 5303 /SAVE FOR TYPEOUT
1362 5303 /RESTORE DATA FIELD
1363 7043 /TO ERROR MONITOR
1364 3147
1365 6201
1366 5310
```

```

1370 7300
1371 4474
1372 7450
1373 5564
1374 3376
1375 4553
1376 0000
1377 7777
1420 7777
1421 7777
1422 5563

/PMODE
/NOW SET UP EXTENDED MEMORY FOR FURTHER TESTING
/
TST23, CLA CLL
JMS I GETBNK
SNA
JMP I TST24N
DCA I*2
JMS I RELOC
0000
7777
7777
7777
7777
JMP I TST23N

/CLEAR AC
/GO FIND NEXT BANK
/DONE?
/YES, EXIT
/NO SAVE BANK FOR EXECUTION
/GO RELOCATE ALL OF MEMORY
/TARGET BANK
/ORG.
/DEST.
/LENGTH
/DO IT AGAIN

```

```

/TRY A CIF-ION-JMP TO ALL BANKS
/
TST24, CLA CLL PPOINT /CLEAR AC
DCA JMS I GETBANK /ZERO THE PMODE SWITCH
SNA JMP TST25 /GO GET THE NEXT BANK
RTL RAL REGB /DONE?
RAL RAL REGB /EXIT
DCA TAD KCIF /JUSTIFY
TAD TAD *5 /JUSTIFY
DCA TAD PNIG /SAVE IT
TAD TAD RETURN /FETCH IT
TSF DCA TSF /ADD CIF
JMS I SETFLG /STORE FOR EXECUTION
0000 ION /GET RETURN ADDRESS
ION JMP /SET UP HANDLER
JMP /FLAG SET?
IOF CIF /NO, GO SET IT
CIF JMP /EXECUTE CIF
JMP /ENABLE INTERRUPTS
IOF CIF /WAIT
CIF JMP /DISABLE INTERRUPTS
JMP I RETURN /BACK TO BANK 0
TAD PREG /JUMP DOWN
AND K0070 /GET INTERRUPT SF
DCA REGC /CLEAR OUT ALL BUT 06,07,08
TAD REGC /SAVE IT
CIA /FETCH IT
TAD REGC /2'S COMPLEMENT
SNA CLA /COMPARE
JMS I ERROR /CHECK MONITOR
TST24M HLT /IF FAILED TO LOAD
SKP /MESSAGE POINTER
TST24+10 /ERROR HALT
JMP TST24 /TO NEXT TEST
/ISZ LOOP/ SCOPE LOOP

```

```
1446 7300 /LMODE
1447 3265 /TRY A LIF-ION-NOP TO ALL BANKS
1450 4474 /TST25, CLA CLL
1451 7450 DCA LPOINT
1452 5314 JMS I GETBNK
1453 3146 SNA TST26
1454 1146 JMP REGB
1455 7006 DCA REGB
1456 1111 TAD RTL
1457 3266 TAD TAD
1460 1132 DCA KLIF
1461 1112 DCA .+7
1462 3050 TAD PNTH
1463 6041 TAD KLJMP
1464 4556 DCA LSET
1465 6141 JMS I SETFLG
1466 0000 LINC
1467 0500 IOB
1470 6001 ION
1471 0016 LNOP
1472 7472 LJMP
1473 2064 , LREG
1474 1560 20
1475 6037 5
1476 0305 ROR
1477 4147 STC
1500 0002 PDP
1501 1146 TAD
1502 7041 CIA
1503 1147 TAD
1504 7650 SNA CLA
1505 4516 JMS I NERROR
1506 4471 JMS I ERROR
1507 6631 TST25M
1510 7402 HLT
1511 7410 SKP
1512 1454 TST25+6
1513 5246 JMP
TST25
```

/CLEAR AC
/ZERO THE LMODE SWITCH
/GET NEXT BANK
/DONE?
/EXIT
/SAVE FIELD
/FETCH IT
/JUSTIFY
/MAKE IT A LIF
/STORE FOR EXECUTION
/GET RETURN ADDRESS
/MAKE IT A LINC JUMP
/STORE FOR RETURN
/FLAG SET?
/NO, GO SET IT
/GO TO LMODE
/EXECUTE LIF

/ENABLE INTERRUPTS
/WAIT
/WAIT
/GET SAVE FIELD
/CLEAR OUT ALL BUT IF

/JUSTIFY
/SAVE IT
/BACK TO PMODE
/GET TARGET IF
/2'S COMPLEMENT
/GET CURRENT IF
/EQUAL?
/CHECK MONITOR
/IF FAILED TO LOAD
/MESSAGE POINTER
/ERROR HALT
/TO NEXT TEST
/ISZ LOOP: SCOPE LOOP

/PMODE

/NOW GO TO EXTENDED MEMORY AND TEST RMF

1514	7300	TST26,	CLA CLL	/CLEAR AC
1515	4474	JMS I	GETBNK	/GET NEXT BANK
1516	7450	SNA		/DONE?
1517	5565	JMP I	TST27N	/YES, NEXT TEST
1520	7006	RTL		/JUSTIFY
1521	7004	RAL		/JUSTIFY
1522	3146	DCA	REGB	/SAVE BANK
1523	6041	TSF		/FLAG SET?
1524	4556	JMS I	SETFLG	/NO, GO SET IT
1525	7040	CMA		/SET AC=7777
1526	3142	DCA	PPOINT	/SET P SWITCH=1
1527	1146	TAD	REGB	/GET BANK
1530	1102	TAD	KCIF	/MAKE IT A CIF N
1531	3332	DCA	.+1	/STORE FOR EXECUTION
1532	0000	ION		/EXECUTE CIF
1533	6001	JMP		/ENABLE INTERRUPTS
1534	5334	JMP		/GO TO EXTENDED MEMORY AND WAIT
1535	6002	IOF		/DISABLE INTERRUPTS
1536	6224	RIF		/GET INSTRUCTION FIELD
1537	3550	DCA I	REGCN	/SAVE IT
1540	6202	CIF	00	/BACK TO FIELD 0
1541	5342	JMP	.+1	/CHANGE FIELDS
1542	7300	CLA CLL	REGB	/GET TARGET FIELD
1543	1146	TAD		/2'S COMPLEMENT
1544	7041	CIA		/COMPARE WITH ACTUAL FIELD
1545	1147	TAD	REGC	/EQUAL?
1546	7650	SNA CLA		/CHECK MONITOR
1547	4516	JMS I	NERROR	/CIF FAILED TO FIND PROPER IF
1550	4471	JMS I	ERROR	/MESSAGE POINTER
1551	6664	TST26M		/ERROR HALT
1552	7402	HLT		/TO NEXT TEST
1553	7410	SKP		/ISZ LOOP/ SCOPE LOOP
1554	1523	TST26+7		/DO NEXT BANK
1555	5314	JMP	TST26	

```

1556 7300 /PMODE
1557 1133 /INTERRUPT INHIBIT TEST BANK 0 - BANK N - BANK 0
1560 3155
1561 4474
1562 7451
1563 5566
1564 3146
1565 1146
1566 7006
1567 7024
1570 1102
1571 3374
1572 6241
1573 4556
1574 4020
1575 7000
1576 5377
1577 7000
1600 7000
1601 6002
1602 6202
1603 5204
1604 4516
1605 4471
1606 6721
1607 7402
1610 7410
1611 1565
1612 5565

TST27, CLA CLL
TAD PNTI
OCA RETURN
JMS I GETBANK
SNA
JMP I TST26N
OCA REG8
TAD REG8
RTL
RAL
TAD
DCA
TSF
JMS I SETFLG
0000
NOP
JMP
NOP
NOP
NOP
LOF
CIF
JMP
JMS I ERROR
JMS I ERROR
TST27M
HLT
SKP
TST27+7
JMP I TST27N

LOC1,
/CLEAR AC
/GET RETURN
/SET UP HANDLER
/GET NEXT BANK
/DONE
/YES, GO TO NEXT TEST
/SAVE BANK
/FETCH IT
/JUSTIFY FOR CIF
/JUSTIFY
/MAKE IT A CIF
/STORE FOR EXECUTION
/FLAG SET?
/NO, GO SET IT
/EXECUTE CIF
/SPACER
/GO TO UPPER MEMORY
/WAIT FOR INTERRUPT
/WAIT FOR INTERRUPT
/TO HERE IF NO INTERRUPT
/BACK TO BANK 0
/JUMP INTO MONITOR
/INTERRUPT OK; CHECK MONITOR
/PMODE INTERRUPT INHIBIT FAILED
/MESSAGE POINTER
/ERROR HALT
/TO NEXT TEST
/ISZ LOOP; SCOPE LOOP
/DO NEXT BANK

```

```
1613 7300 /LMODE
1614 3065 /INTERRUPT INHIBIT TEST BANK 0 -BANK N- BANK 2
1615 1134 /
1616 1112 TST28, CLA CLL
1617 3050 DCA LPOINT
1620 4474 PNTJ
1621 7452 KLJMP
1622 5263 LSET
1623 3146 GETBNK
1624 1146 JMS I
1625 7006 SNA
1626 1111 JMP
1627 3233 DCA
1630 6041 TAD
1631 4556 REGB
1632 6141 TAD
1633 0607 RTL
1634 0500 TAD
1635 6001 DCA
1636 7637 TSF
1637 0016 JMS I
1640 0600 LINC
1641 0507 LIF
1642 6001 IOB
1643 7644 ION
1644 0016 LJMP
1645 0500 LNOP
1646 6002 IOB
1647 0002 IOF
1650 5254 PDP
1651 0002 JMP
1652 6002 PDP
1653 7410 IOF
1654 4516 SKP I
1655 4471 JMS I
1656 6756 JMS I
1657 7402 TST28M
1660 7610 HLT
1661 1624 SKP CLA
1662 5213 TST28+11
JMP TST28
```

LOCJ,

```

/CLEAR AC
/CLEAR HANDLER SWITCH
/GET ERROR RETURN
/MAKE IT A LINC JUMP
/PLACE IT IN HANDLER
/GET NEXT 4K BANK
/DONE?
/YES, NEXT TEST
/SAVE BANK
/FETCH IT
/JUSTIFY FOR LMODE LIF
/MAKE IT A LIF N
/STORE FOR EXECUTION
/FLAG SET?
/NO, GO SET IT
/TO LINC MODE
/EXECUTE LIF N
/
/ENABLE INTERRUPTS (SHOULD INHIBIT)
/TO EXTENDED MEMORY
/WAIT FOR INTERRUPT
/LOAD IB
/
/ENABLE INTERRUPT AGAIN
/BACK TO BANK 0
/WAIT FOR INTERRUPT
/
/DISABLE INTERRUPT
/BACK TO PMODE
/TO NON-ERROR
/BACK HERE IF INTERRUPT OCCURS
/DISABLE INTERRUPT
/SKIP INTO ERROR
/CHECK MONITOR
/LIF FAILED TO INHIBIT INTERRUPT
/MESSAGE POINTER
/ERROR HALT
/TO NEXT TEST
/ISZ LOOP; SCOPE LOOP
/NEXT BANK
```

```

1663 7300 /LMODE
1664 3065 /INTERRUPT INHIBIT TEST; DOES JMP 0 CLEAR INT INH?
1665 1135 /
1666 1112 TST29, CLA CLL
1667 3050 OCA LPOINT
1670 4474 TAD PNTK
1671 7450 TAD KLJMP
1672 5347 DCA LSET
1673 3146 JMS I GETBNK
1674 1146 SNA
1675 7006 JMP
1676 1111 DCA REGB
1677 3325 TAD REGB
1678 6041 RTL
1679 4556 DCA EX29
1700 3014 TAD SETFLG
1701 1146 DCA PINT
1702 7006 TAD REGB
1703 7004 RAL
1704 1101 TAD KODF
1705 3310 ,+1
1706 1100 CML
1707 7020 TAD
1708 1105 DCA I
1709 3414 TAD
1710 1106 DCA I
1711 1325 TAD
1712 3414 DCA I
1713 1050 TAD LSET
1714 3414 DCA I
1715 6201 CDF
1716 1107 TAD
1717 6141 LINC
1718 3000 EX29,
1719 1500
1720 6001 ION
1721 7731 LNOP
1722 1016 LNOP
1723 4000 STC
1724 6000 LUMP
1725 1100
1726 1100
1727 6001
1728 7731
1729 1016
1730 4000
1731 6000
1732 1100
1733 1100

/CLEAR AC
/SET L SWITCH TO OFF
/GET RETURN
/MAKE IT A LINC JUMP
/PUT IT IN HANDLER
/GET NEXT BANK
/DONE?
/YES, NEXT TEST
/SAVE TARGET
/FETCH IT
/JUSTIFY FOR LIF
/MAKE IT A LIF N
/STORE FOR EXECUTION
/FLAG SET?
/NO, GO SET IT
/SET UP AUTO-INDEX
/GET TARGET
/JUSTIFY FOR CDF
/JUSTIFY
/MAKE IT A CDF N
/STORE FOR EXECUTION
/EXECUTE CDF
/SET LINK
/GET IOB
/CELL 0001 BANK N
/GET IOF
/CELL 0002, BANK N
/GET LIF N
/CELL 0003, BANK N
/GET LUMP LOCK
/CELL 0004, BANK N
/RESTORE DF
/GET NOP
/TO L MODE
/EXECUTE LIF

/TO UPPER MEMORY
/WAIT FOR INTERRUPT
/SET UP 0
/JMP 0

```


1734	0002	LOCK,	PDP		/TO HERE AFTER INTERRUPT OR JMP 0
1735	6202		CIF	00	/BACK TO BANK 0 IF NOT THERE
1736	5337		JMP	.*+1	/TO LOWER MEMORY
1737	7430		SZL		/SKIP MEANS INTERRUPT NOT INHIBITED
1740	4516		JMS	1	/CHECK MONITOR
1741	4471		JMS	1	/JMP 0 INT INH IN ERROR
1742	7013		TST29M		/MESSAGE POINTER
1743	7402		HLT		/ERROR HALT
1744	7410		SKP		/TO NEXT TEST
1745	1674		TST29+11		/ISZ LOOP/ SCOPE LOOP
1746	5263		JMP	TST29	/NEXT BANK

```

1747 7340 /LMODE
1750 3065 /WILL DJR-JMP 0 LOAD THE IF?
1751 1136
1752 2036
1753 1112
1754 3050
1755 4474
1756 7450
1757 5970
1760 7006
1761 7001
1762 3146
1763 1146
1764 1111
1765 3541
1766 1036
1767 3014
1770 1146
1771 7004
1772 121
1773 1101
1774 3375
1775 0000
1776 1105
1777 7412
2000 0000
2001 3414
2002 1110
2003 3414
2004 1050
2005 3414
2006 6141
2007 0000
2010 0006
2011 6000

1747 7340 /LMODE
1750 3065 /WILL DJR-JMP 0 LOAD THE IF?
1751 1136
1752 2036
1753 1112
1754 3050
1755 4474
1756 7450
1757 5970
1760 7006
1761 7001
1762 3146
1763 1146
1764 1111
1765 3541
1766 1036
1767 3014
1770 1146
1771 7004
1772 121
1773 1101
1774 3375
1775 0000
1776 1105
1777 7412
2000 0000
2001 3414
2002 1110
2003 3414
2004 1050
2005 3414
2006 6141
2007 0000
2010 0006
2011 6000

TST30, CLA CLL CMA /CLEAR AC
DCA LPOINT /SWITCH=1
TAD PNTL /GET RETURN
AND K1777 /CLEAR BITS 0,1
TAD KLJMP /MAKE IT A LINC JUMP
DCA LSET /PUT IT IN HANDLER (WE WON'T USE INTERRUPTS)
JMS I GET8BK /GET NEXT BANK
SNA I /DONE?
JMP I TST32N /YES, NEXT TEST
RTL /JUSTIFY FOR LIF
DCA IAC /ADD CURRENT IF
DCA DCA REG8
TAD REG8
TAD KLIF
DCA I PNT32N
TAD K1777
DCA PINT
TAD REG8
RAL PMASK
AND KCOF
TAD .+1
DCA K10B
0000
TAD SKP
0000
DCA I PINT
TAD KRIF
DCA I PINT
TAD LSET
DCA I PINT
LINC
0000
DCA I PINT
DJR LUMP
LJMP 0

/MAKE IT A LIF N
/STORE FOR EXECUTION
/SET AC = 1777 FOR IF 01
/SET UP AUTO-INDEX
/GET TARGET
/JUSTIFY FOR CDF
/JUSTIFY
/MAKE IT A CDF N
/STORE FOR EXECUTION
/EXECUTE CDF N
/IOB
/WASTE A SPACE
/LINC JUMP SAVE
/CELL 2000, BANK N
/RIF LINC MODE (5 BITS)
/CELL 2001, BANK N
/LJMP LOCL
/CELL 2002, BANK N
/TO LMODE
/EXECUTE LIF N
/DISABLE JUMP RETURN SAVE
/JMP 0

```

2012	0601	LOCL,	LIF	1	/WE WILL ALWAYS BE IN UPPER MEM
2013	6014		LJMP	,+1	/BACK TO LOWER MEMORY
2014	0002		PDP		/BACK TO PMODE
2015	7010		RAR		/JUSTIFY
2016	3147		DCA	REGC	/SAVE FIRST RIF
2017	6201		ODF	0	/RESTORE OF
2020	1146		TAD	REG8	/GET TARGET
2021	7041		CIA		/2'S COMPLEMENT
2022	1147		TAD	REGC	/FETCH IF
2023	7650		SNA CLA		/DID WE LOAD THE IF?
2024	4516		JMS I	NERROR	/CHECK MONITOR
2025	4471		JMS I	ERROR	/OJR-JMP 0 FAILED TO LOAD IF
2026	7054		TST30M		/MESSAGE POINTER
2027	7402		HLT		/ERROR HALT
2030	7410		SKP		/TO NEXT TEST
2031	1763		TST30X		/ISZ LOOP; SCOPE LOOP
2032	5567		JMP I	TST30N	/NEXT BANK

```

2033 7340 /LMODE
2034 3065 /WILL ION-LIF INHIBIT INTERRUPTS?
2035 4474 /TIMING RACE IF EP12-00018 IS NOT INSTALLED)
2036 7450 TST32, CLA CLL CMA /SET AC = 7777
2037 5272 DCA L LPOINT /SET SWITCH
2038 3146 JMS I GETBNK /GET NEXT BANK
2039 6041 SNA /DONE?
2040 4556 JMP TST33 /YES, NEXT TEST
2041 1146 DCA REGB /SAVE BANK
2042 7021 TSF JMS I SETFLG
2043 1146 JMS I SETFLG
2044 7021 JMS I SETFLG
2045 7021 JMS I SETFLG
2046 1111 IAC CML KLIFF
2047 3253 DCA ,+4
2048 6141 LINC
2049 0500 IOB
2050 6001 ION
2051 0000 LNOP
2052 0016 LNOP
2053 0016 LNOP
2054 0016 IOB
2055 6002 IOF
2056 0001 LIF
2057 0002 PDP
2058 0002 SEL
2059 7437 JMS I ERROR
2060 4516 JMS I ERROR
2061 0002 TST32M
2062 7437 HLT
2063 4471 SKP
2064 7106 TST32X
2065 7402 JMP
2066 7402 TST32
2067 741
2068 2043
2069 5233

```

```

2072 7301 /LMODE
2073 4474 /DOES LIF CAUSE THE IF/DF TO TRANSFER TO THE SF?
2074 7450 /
2075 5572 TST33, CLA CLL
2076 3146 JMS I GETBANK
2077 6041 SNA I
2078 4556 JMP I TST34N
2079 1146 DCA REGC
2080 7006 TSF
2081 1146 JMS I SETFLG
2082 7006 TAD REGC
2083 7006 RTL
2084 3147 IAC
2085 1147 DCA
2086 7040 TAD
2087 1114 CMA
2088 3152 AND
2089 1152 DCA
2090 1104 TAD
2091 3472 TAD
2092 1147 DCA I
2093 3473 TAD
2094 6141 DCA I
2095 1111 LINC
2096 3473 XDF33,
2097 1111 XIF33,
2098 6123
2099 1111
2100 3473
2101 1111
2102 3473
2103 1111
2104 3473
2105 1111
2106 3473
2107 1111
2108 3473
2109 1111
2110 3473
2111 1111
2112 3473
2113 1111
2114 3473
2115 1111
2116 3473
2117 1111
2118 3473
2119 1111
2120 3473
2121 1111
2122 3473
2123 1111
2124 3473
2125 1111
2126 3473
2127 1111
2128 3473
2129 1111
2130 3473
2131 1111
2132 3473
2133 1111
2134 3473
2135 1111
2136 3473
2137 1111
2138 3473
2139 1111
2140 3473
2141 1111
2142 3473
2143 1111
2144 3473
2145 1111
2146 3473
2147 1111
2148 3473
2149 1111
2150 3473
2151 1111

```

/CLEAR AC
 /GET NEXT BANK
 /DONE?
 /YES, NEXT TEST
 /SAVE BANK
 /FLAG SET?
 /NO, GO SET IT
 /GET BANK
 /JUSTIFY FOR LIF/LDF
 /GET CURRENT IF
 /SAVE IT
 /FETCH IT
 /COMPLEMENT
 /SAVE DF BITS
 /SAVE IT
 /FETCH IT
 /MAKE IT A LDF -N
 /STORE FOR EXECUTION
 /FETCH CONSTANT
 /MAKE IT A LIF N
 /STORE FOR EXECUTION
 /TO LMODE
 /EXECUTE LDF
 /EXECUTE LIF
 /TO UPPER MEMORY
 /RESTORE IF
 /
 /READ SF
 /RESTORE OF
 /BACK TO BANK 0
 /JUSTIFY
 /TO PMODE
 /SAVE RIB DATA
 /GET IF
 /JUSTIFY
 /
 /
 /GET OF
 /2'S COMPLEMENT
 /ADD RECEIVED
 /EQUAL?
 /CHECK MONITOR
 /LIF FAILED TO LOAD SF
 /MESSAGE POINTER
 /ERROR HALT
 /TO NEXT TEST
 /ISE LOOP; SCOPE LOOP
 /NEXT BANK

```

2152 7320  TST34, CLA CLL
2153 4474  JMS I GETBNK
2154 7450  SNA
2155 5573  JMP I TST35N
2156 3146  DCA REGB
2157 1146  TAD REGB
2160 7006  RTL
2161 7001  IAC
2162 1111  TAD
2163 3366  DCA KLIF
2164 6002  IOF
2165 6141  LINC
2166 0000  JMS I
2167 6170  LJMP
2170 0500  IOB
2171 6244  RMF
2172 0500  IOB
2173 6234  RIB
2174 6175  LJMP
2175 0002  PDP
2176 3550  DCA I REGCN
2177 6224  RIF
2200 7640  SZA CLA
2201 7040  CMA
2202 6212  CIF
2203 5204  JMP
2204 7650  SNA CLA
2205 4516  JMS I NERROR
2206 4471  JMS I ERROR
2207 7201  TST34M
2210 7402  HLT
2211 7410  SKP
2212 2157  TST34+5
2213 5572  JMP I TST34N

/ LMODE
/ WILL RMF WORK IN EXTENDED MEMORY?
/
/ CLEAR AC
/ GET NEXT BANK
/ DONE?
/ YES, NEXT TEST
/ SAVE TARGET
/ FETCH IT
/ JUSTIFY FOR LIF
/ INCREMENT FOR FIELD 2
/ MAKE IT A LIF N
/ STORE FOR EXECUTION
/ DISABLE INTERRUPTS
/ TO LMODE
/ EXECUTE LIF N
/ TO UPPER MEMORY
/
/ RESTORE MEMORY
/
/ FIND OUT WHERE WE ARE
/ TO LOWER MEMORY
/ TO PMODE
/ SAVE TARGET - DATA FIELD IS ZERO
/ NOW WHERE ARE WE?
/ RMF FAILED IF NOT ZERO
/ SET AC=7777 TO CAUSE ERROR
/ JUST TO BE SURE
/ BACK TO BANK 0
/ AC=7777 IF ERROR
/ CHECK MONITOR
/ RMF FILED
/ MESSAGE POINTER
/ ERROR HALT
/ TO NEXT TEST
/ ISZ LOOP; SCOPE LOOP
/ NEXT BANK

```

```

2430      *2400
2431      /PMODE
2432      /AUTO INDEX TEST (FIRST SET UP REGISTERS)
2433      /
2434      TST35, CLA CLL      /CLEAR AC
2435      JMS I GETBNK      /GET NEXT BANK
2436      SNA      /DONE?
2437      JMP TST36X      /NEXT TEST
2438      DCA REGB      /SAVE IT
2439      TAD      /FETCH IT
2440      RTL      /JUSTIFY
2441      RAL      /JUSTIFY
2442      TAD      /MAKE IT A CIF N
2443      DCA ,+1      /STORE FOR EXECUTION
2444      DCA REGB      /EXECUTE CIF
2445      RTL      /GET BANK
2446      RAL      /JUSTIFY
2447      TAD      /JUSTIFY
2448      DCA ,+1      /MAKE IT A CDF N
2449      DCA REGB      /STORE FOR EXECUTION
2450      JMP ,+1      /EXECUTE CDF
2451      DCA ,+1      /TO UPPER MEMORY
2452      DCA 0      /CLEAR 0
2453      DCA I END      /COMPLEMENT AC
2454      CMA      /SET END (END=7777)
2455      DCA 10      /NOW SET AUTO 10-17 TO 7777
2456      DCA 11
2457      DCA 12
2458      DCA 13
2459      DCA 14
2460      DCA 15
2461      DCA 16
2462      DCA 17
2463      DCA 17

```

/NOW TEST REGISTERS
 /

2445	1410	TAD I	10		
2446	7640	SZA CLA			/FETCH INDIRECT INDEXING TO 0
2447	5305	JMP	ERR10		/ZERO?
2450	1411	TAD I	11		/TO ERROR LOOP.
2451	7640	SZA CLA			
2452	5304	JMP	ERR11		
2453	1412	TAD I	12		
2454	7640	SZA CLA			
2455	5303	JMP	ERR12		
2456	1413	TAD I	13		
2457	7640	SZA CLA			
2460	5302	JMP	ERR13		
2461	1414	TAD I	14		
2462	7640	SZA CLA			
2463	5301	JMP	ERR14		
2464	1415	TAD I	15		
2465	7640	SZA CLA			
2466	5300	JMP	ERR15		
2467	1416	TAD I	16		
2470	7640	SZA CLA			
2471	5277	JMP	ERR16		
2472	1417	TAD I	17		
2473	7640	SZA CLA			
2474	5276	JMP	ERR17		
2475	5316	JMP	OK35		/THIS BANK IS OK


```

2476 7001 ERR17, IAC /NOW HANDLE THE RETURN
2477 7001 ERR16, IAC /
2500 7001 ERR15, IAC /INCREMENT AC TO FAILING CELL
2501 7001 ERR14, IAC
2502 7001 ERR13, IAC
2503 7001 ERR12, IAC
2504 7001 ERR11, IAC
2505 7001 ERR10, IAC
2506 6202 CIF
2507 5011 JMP
2510 1023 TAD
2511 3147 DCA
2512 1547 TAD I
2513 3151 DCA
2514 6201 CDF
2515 5322 JMP
2516 6201 CDF
2517 6202 CIF
2520 5321 JMP
2521 4516 JMS I
2522 4471 JMS I
2523 7236 TST35M
2524 7402 HLT
2525 7611 SKP CLA
2526 2405 TST35+5
2527 5200 JMP
2530 7340 CLA CLL
2531 3145 DCA
2532 5733 JMP I
2533 2600 TST36
2534 7777 TST37
END, 7777

0000 /BACK TO BANK 0
0001 /TO LOWER MEMORY
0002 /ADD JUSTIFICATION
0003 /SAVE FAILING CELL
0004 /GET CONTENTS
0005 /SAVE IT
0006 /RESTORE DATA FIELD
0007 /TO ERROR MONITOR
0008 /RESTORE DATA FIELD
0009 /RESTORE INSTRUCTION FIELD
0010 /TO LOWER MEMORY
0011 /CHECK MONITOR
0012 /AUTO INDEX FAILED
0013 /MESSAGE POINTER
0014 /ERROR HALT
0015 /TO NEXT BANK
0016 /SCOPE LOOP
0017 /NEXT BANK
0018 /SET AC=7777
0019 /PRESET REGA
0020 /TO NEXT TEST
0021 /DONE THIS WAY TO AVOID PAGING ERRORS)

```

```

2600      *2600
2601      /LMODE
2602      /AUTO INDEX TEST
2603      /
2604      TST36, CLA CLL
2605      JMS I GETBNK
2606      SNA
2607      JMP ,+6
2608      DCA ,+3
2609      REG
2610      JMS I BNKSET
2611      0001
2612      JMP TST36
2613      JMS I GETBNL
2614      SNA CLL
2615      JMP GOAUTO
2616      DCA REG
2617      TAD REG
2618      RAR
2619      CLL
2620      AND KLJMP
2621      TAD K0017
2622      DCA DEST36
2623      TAD REG
2624      RTR
2625      AND K0077
2626      DCA ,+2
2627      JMS I RELOC
2628      0003
2629      LAUTO-1
2630      0000
2631      LEND-LAUTO
2632      JMS I NSBK
2633      JMS I GETBNL
2634      SNA
2635      JMP I PASSN
2636      DCA REG
2637      TAD REG
2638      KLIF
2639      DCA ,+10
2640      TAD REG
2641      KLOF
2642      DCA ,+6
2643      TAD K0020
2644      TAD KLJMP
2645      DCA ,+4
2646      EXAUT, LINC
2647      0000
2648      0000
2649      0000
2650      0000

2601      /CLEAR AC
2602      /FIND NEXT BANK
2603      /DONE?
2604      /YES, RELOCATE
2605      /SAVE BANK
2606      /ZERO REG
2607      /ZERO BANK
2608      /TARGET BANK TO BE SET TO ZERO
2609      /NEXT BANK
2610      /GET NEXT LINC FIELD
2611      /DONE?
2612      /YES, START TESTING
2613      /SAVE IT
2614      /FETCH IT
2615      /JUSTIFY FOR IF BITS 3 & 4 TO MA 0,1
2616      /JUSTIFY
2617      /CLEAR LINK
2618      /CLEAR BITS 2-11
2619      /ADD 17; THIS WILL BE THE TARGET ADDRESS=1
2620      /STORE
2621      /GET BANK
2622      /JUSTIFY
2623      /CLOSE ENOUGH
2624      /STORE
2625      /GO RELOCATE PROGRAM
2626      /BANK
2627      /ORG
2628      /DEST.
2629      /LENGTH
2630      /NEXT FIELD
2631      /FIND TEST
2632      /DONE?
2633      /GO TYPE PASS ALARM
2634      /SAVE TARGET
2635      /FETCH IT
2636      /MAKE IT A LIF N
2637      /STORE FOR EXECUTION
2638      /FETCH TARGET
2639      /MAKE IT A LOF N
2640      /STORE FOR EXECUTION
2641      /GET 20
2642      /MAKE A LUMP 20
2643      /STORE FOR EXECUTION
2644      /TO LMODE
2645      /LIF N
2646      /LOF N
2647      /JMP 20

```

```

/
/TO HERE IN PHODE IF INDEX OK
/
2557 6201 CDF JMS I 00 /CHECK MONITOR
2558 4516 JMS I 00 /AUTO-INDEX FAILED (DIRECT TO HERE FROM ERROR)
2559 4471 JMS I 00 /MESSAGE POINTER
2560 7272 TST36M HLT /ERROR HALT
2561 7422 HLT /TO NEXT FIELD
2562 7410 SKP /SCOPE LOOP
2563 2653 EXAUT /NEXT FIELD,
2564 2653 JMP GOAUTO
2565 5236

```

422	*4020	/LMODE (THIS PORTION IS RELOCATED FOR EACH FIELD)	
		/AUTO INDEX TEST (IF=OF)	
		/	
		LAUTO, SET 1 /SET UP REGISTERS	
4020	0061	SET 3777	
4021	3777	SET 3777	
4022	0062	SET 3777	
4023	3777	SET 3777	
4024	0063	SET 3777	
4025	3777	SET 3777	
4026	0064	SET 3777	
4027	3777	SET 3777	
4030	0065	SET 3777	
4031	3777	SET 3777	
4032	0066	SET 3777	
4033	3777	SET 3777	
4034	0067	SET 3777	
4035	3777	SET 3777	
4036	0070	SET 3777	
4037	3777	SET 3777	
4040	0071	SET 3777	
4041	3777	SET 3777	
4042	0072	SET 3777	
4043	3777	SET 3777	
4044	0073	SET 3777	
4045	3777	SET 3777	
4046	0074	SET 3777	
4047	3777	SET 3777	
4050	0075	SET 3777	
4051	3777	SET 3777	
4052	0076	SET 3777	
4053	3777	SET 3777	
4054	0077	SET 3777	
4055	3777	CLR	
4056	0011	STC	
4057	4000	LDA	
4060	1020	5252	
4061	5252	STA	
4062	1040	1777	
4063	1777		
		/SET 0=0000	
		/PICK UP CONSTANT	
		/SET 1777=5252	

/NOW TEST THE REGISTERS

4064	1011	CLR	21	/GET INDIRECT INDEX 0
4065	1021	LDA		/#ZERO?
4066	1050	AZE	ERL1	/AUTO INDEX FAILED
4067	6172	LJMP	22	
4070	1022	LDA		
4071	1050	AZE	ERL2	
4072	6184	LJMP	23	
4073	1023	LDA		
4074	1050	AZE	ERL3	
4075	6163	LJMP	24	
4076	1024	LDA		
4077	1050	AZE	ERL4	
4100	6162	LJMP	25	
4101	1025	LDA		
4102	1050	AZE	ERL5	
4103	6161	LJMP	26	
4104	1026	LDA		
4105	1050	AZE	ERL6	
4106	6160	LJMP	27	
4107	1027	LDA		
4110	1050	AZE	ERL7	
4111	6157	LJMP	30	
4112	1030	LDA		
4113	1050	AZE	ERL10	
4114	6156	LJMP	31	
4115	1031	LDA		
4116	1050	AZE	ERL11	
4117	6155	LJMP	32	
4120	1032	LDA		
4121	1050	AZE	ERL12	
4122	6154	LJMP	33	
4123	1033	LDA		
4124	1050	AZE	ERL13	
4125	6153	LJMP	34	
4126	1034	LDA		
4127	1050	AZE	ERL14	
4130	6152	LJMP	35	
4131	1035	LDA		
4132	1050	AZE	ERL15	
4133	6151	LJMP	36	
4134	1036	LDA		
4135	1050	AZE	ERL16	
4136	6150	LJMP	37	
4137	1037	LDA		
4140	1050	AZE	ERL17	
4141	6147	LJMP		

```

4142 0002 PDP /AUTO OK
4143 6201 CDF 02
4144 6202 CDF 02
4145 5746 JMP I ,+1
4146 2657 LOK
4147 0221 ERL17, XSK 1
4150 0221 ERL16, XSK 1
4151 0221 ERL15, XSK 1
4152 0221 ERL14, XSK 1
4153 0221 ERL13, XSK 1
4154 0221 ERL12, XSK 1
4155 0221 ERL11, XSK 1
4156 0221 ERL10, XSK 1
4157 0221 ERL7, XSK 1
4160 0221 ERL6, XSK 1
4161 0221 ERL5, XSK 1
4162 0221 ERL4, XSK 1
4163 0221 ERL3, XSK 1
4164 0221 ERL2, XSK 1
4165 0221 XSK 1
4166 0016 LNOP
4167 0016 LNOP
4170 0016 LNOP
4171 6174 JMP ,+3
4172 0061 SET 1
4173 0001 ERL1, 1
4174 0001 LDA
4175 0021 21
4176 0002 PDP
4177 6201 CDF 02
4178 3604 DCA I PONT
4179 6202 CDF 02
4180 5603 JMP I ,+1
4183 2661 ERL36
4184 0147 PONT,
4185 0000 LEND,

```

/RETURN FOR NEXT BANK
/INCREMENT ERROR POINTER
/NONE OF THESE WILL SKIP.

/WASTE SOME SPACE FOR PAGING REASONS

/MUST BE CELL 1 THAT FAILED

/FETCH IT

/TO PMODE
/RESTORE DF
/SAVE ERROR
/RESTORE IF
/TO BANK 0
/RETURN
/ERROR POINTER
/END POINTER

/ALERT OPERATOR OF PASS COMPLETION (INHIBIT IF RSW 06=21)

```

PASS,      CLA CLL      REGA      /CLEAR REGA
DCA        00          COUNT      /INCREMENT COUNT
COF        00          /DON'T SKIP
ISZ        00          /GET SWITCHES
NOP        00          /PICK OUT BIT 06
LAS        00          /SET ?
AND        00          /YES, INHIBIT AND RESTART
SZA CLA     177        /GET POINTER TO TEXT
JMP        PNT0        /CHEAT MONITOR
TAD        ERROR       /GO TYPE MESSAGE
DCA I      PASPNT      /MESSAGE POINTER
JMP I      /LINKUP POINTER
TST37M
LOCO,
PASPNT, ASCII

```

```

4225 730
4227 3145
4229 6201
4231 2017
4233 7000
4235 7604
4237 1026
4239 7641
4241 5177
4243 1137
4245 3471
4247 5623
4249 7323
4251 5051

```

```

5000      *5000
5001      /
5002      /NON ERROR MONITOR DETERMINES IF OPERATOR WANTS TO LOOP ON NON FAILING TEST
5003      /RETURN ADDRESS
5004      /SET AC = 4
5005      /GET RETURN ADDRESS
5006      /RETURN ADDRESS +4
5007      /GET SCOPE LOOP ADDRESS
5008      /STORE IT
5009      /UPDATE DATA
5010      /LOOP BACK TO TEST
5011      /READ SWITCHES
5012      /SAVE SR3
5013      /TEST AND CLEAR
5014      /LOOPING
5015      /SET AC=-1
5016      /ADD NERROS
5017      /STORE IN NERROS
5018      /JUMP INDIRECT LOOP
5019      /
5020      /ERROR PROCESSOR, SCOPE LOOP, HALT, PRINT
5021      /RETURN ADDRESS STORAGE
5022      /READ SWITCHES
5023      /MOVE SR1 INTO AC00
5024      /IS IT SET
5025      /NO TYPE A MESSAGE
5026      /GET CURRENT ERROR ADDRESS
5027      /INVERT IT
5028      /STORE IN LAST ERROR
5029      /YES INDEX ESCAPE
5030      /READ SWITCHES
5031      /IS SR0 SET?
5032      /NO, ERROR HALT
5033      /YES INDEX ESCAPE TO JUMP OUT
5034      /INDEX ERRORS TO SCOPE MODE
5035      /GET SCOPE ADDRESS
5036      /STORE IN TYPE
5037      /READ SWITCHES
5038      /MOVE SR02 TO AC0
5039      /IS SCOPE MODE SELECTED
5040      /YES CONTINUE IN SCOPE LOOP
5041      /NO SET AC=7777
5042      /SUBTRACT ONE FROM ERRORS
5043      /STORE SELECTED ADDRESS
5044      /EXIT TO NEXT TEST
5045
5000      CLA CLL IAC RTL
5001      TAD NERROS
5002      DCA NERROS
5003      TAD I NERROS
5004      DCA I NERROS
5005      ISZ HEGA
5006      JMP I ERRORS
5007      LAS
5008      AND K0400
5009      SZA CLA
5010      JMP I ERRORS
5011      CMA
5012      TAD NERROS
5013      DCA NERROS
5014      JMP I NERROS
5015
5020      ASCRXT, TAD ASCII
5021      TAD ERRORS
5022      CIA
5023      DCA LSTERR
5024      ISZ ERRORS
5025      LAS
5026      SMA CLA
5027      HLT
5028      ISZ
5029      ISZ
5030      TAD I
5031      DCA NERROS
5032      LAS
5033      SPA CLA
5034      JMP I NERROS
5035      CMA
5036      TAD ERRORS
5037      DCA ERRORS
5038      JMP I ERRORS
5039
5040      SPA CLA
5041      JMP I NERROS
5042      CMA
5043      TAD ERRORS
5044      DCA ERRORS
5045      JMP I ERRORS

```



```

5050 7240 /SET C(AC)=-1
5051 162 /GET MESSAGE ADDRESS STORAGE
5052 3014 /STORE IT IN AUTO INDEX REGISTER
5053 122 /GET RETURN ADDRESS
5054 1115 /SUBTRACT LAST ERROR ADDRESS
5055 7053 /TEST
5056 5362 /SAME GO TYPE DATA
5057 1414 /GET FIRST CHARACTER
5058 3203 /SAVE IT
5059 1203 /GET IT
5060 7450 /TEST IT
5061 5225 /NUMBER=EXIT
5062 7040 /INVERT IT
5063 7450 /NUMBER=EXITA
5064 5314 /TYPE OUT DATA ROUTINE
5065 7040 /CHANGE IT BACK
5066 7112 /SWAP AC TO THE RIGHT
5067 7012 /MOVE
5068 7012 /MOVE
5069 4277 /TYPE IT
5070 1201 /GET IT AGAIN
5071 4277 /TYPE IT
5072 5257 /MUST BE MORE WORDS THAT NEED TYPING
5073 5000 /SAVE SIGNIFICANT PART
5074 3157 /STORE WORD
5075 1157 /FETCH IT
5076 765 /TEST FOR 00 CRLF CODE
5077 4353 /YES IT WAS
5078 1157 /NO TYPE IT
5079 1377 /SUBTRACT 40
5080 7510 /TEST POLARITY
5081 1031 /ADD 340
5082 1376 /ADD 240
5083 4574 /TYPE
5084 5677 /EXIT

```

5114	1414	DATUM,	TAD I	PINT	/GET ADDRESS OF REGISTER
5115	3215	DCA		NERROS	/STORE IN TEMP
5116	1200	TAD		NERROS	/GET TEMP
5117	7650	SNA	CLA		/TEST FOR EXIT
5120	5225	JMP		ASCRXT	/EQUALS 0000 EXIT
5121	1200	TAD		NERROS	/GET TEMP
5122	1373	TAD		M4444	/ADD CONSTANT
5123	7650	SNA	CLA		/TEST FOR RESTART
5124	4467	JMS	I	BELL	/IT'S THERE! RESTART
5125	1600	TAD	I	NERROS	/GET DATA
5126	4332	JMS		OCTYP	/TYPE IT
5127	1376	TAD		K240	/SPACE
5130	4574	JMS	I	TYPE	/TYPE IT
5131	5314	JMP		DATUM	/TYPE NUMERIC DATA
5132	0000	OCTYP,	0		/RETURN ADDRESS STORAGE
5133	3277	DCA		TYPECH	/STORE DATA TO BE PRINTED
5134	1120	TAD		K7774	/SET UP TALLY
5135	3157	DCA		SPACE	/SET IT

5136	1035	HERE,	TAD	K1026	/GET FLAG NUMBER
5137	3353	REDO,	DCA	CRLF	/STORE
5140	1277		TAD	TYPECH	
5141	7004		RAL		
5142	3277		DCA	TYPECH	
5143	1353		TAD	CRLF	
5144	7004		RAL		
5145	7420		SNL	REDO	
5146	5337		JMP	TYPE	
5147	4574		JMS I	SPACE	
5150	2157		ISZ	HERE	
5151	5336		JMP	OCTYP	
5152	5732		JMP I		
5153	0000		0		
5154	1374		TAD	K0215	
5155	4574		JMS I	TYPE	
5156	1375		TAD	K0212	
5157	4574		JMS I	TYPE	
5160	1032		TAD	K0177	
5161	5753		JMP I	CRLF	
5162	1414		TAD I	PINT	
5163	7450		SNA	ASCRXT	
5164	5225		JMP		
5165	7040		CMA		
5166	7640		SZA CLA		
5167	5362		JMP	DATYP	
5170	4353		JMS	CRLF	
5171	7300		CLA CLL		
5172	5314		JMP	DATUM	
5173	3334		-4444		
5174	0215		K 0215		
5175	0212		K 0212		
5176	0240		K 0240		
5177	7740		M40		
			-42		

/EXIT
 /RETURN ADDRESS STORAGE

/GET CR
 /TYPE IT
 /GET LF
 /TYPE IT
 /SET TO RUBOUT

/EXIT
 /GET A TERM OFF OF TYPE LIST
 /END OF LIST?
 /YES EXIT
 /INVERT
 /BEGINNING OF DATA

/NO
 /YES OK RETURN THE TTY CARRIAGE AND LINE FEED
 /CLEAR AC AND LINK
 /GO TYPE THE DATA
 /SWITCH CHECK


```

/PROGRAM RELOCATOR
/CALL: RELOC; BANK, ORG-1, DEST-1, END-ORG.
/
RELOC, 2000 CLA CLL
5261 4000 TAD RELOC
5262 7300 DCA 00
5263 1261 DCA REGB
5264 6201 DCA REGC
5265 3146 DCA REGB
5266 1546 DCA REGC
5267 3147 DCA REGB
5270 2146 ISZ REGB
5271 1546 TAD I
5272 3015 DCA AUT011
5273 2146 ISZ REGB
5274 1546 TAD I
5275 3016 DCA AUT012
5276 2146 ISZ REGB
5277 1546 TAD I
5280 7047 CMA
5301 3151 DCA REGD
5302 2146 ISZ REGB
5303 1146 TAD REGB
5304 3261 DCA RELOC
5305 1147 TAD REGC
5306 7006 RTL
5307 7004 RAL
5310 1121 AND
5311 1101 TAD
5312 3323 DCA EXCREL
5313 2151 ISZ REGD
5314 5321 JMP PICKUP
5315 3146 DCA REGB
5316 3147 DCA REGC
5317 6201 CDF 00
5320 5661 JMP I
5321 6201 CDF 20
5322 1415 TAD I
5323 1000 EXCREL, 4000
5324 3416 DCA I
5325 5313 JMP INCREL

/CONTAINS CALLING LOCATION +1
/CLEAR AC
/GET BANK ADDRESS
/RESET DATA FIELD
/SAVE ADDRESS
/BANK
/SAVE IT
/INCREMENT
/ORG-1
/SAVE IT
/INCREMENT
/DEST-1
/SAVE IT
/INCREMENT
/LENGTH
/COMPLEMENT
/SAVE IT
/INCREMENT
/GET RETURN
/SAVE RETURN
/GET BANK
/JUSTIFY
/SOME MORE
/SAVE BITS 06-08
/GET CDF
/SAVE INSTRUCTION FOR EXECUTION
/CHECK IF DONE
/NOT DONE; MOVE A WORD
/RESET REGISTER
/RESET REGISTER
/RESET DATA FIELD
/RETURN
/RESET DATA FIELD
/GET WORD
/CHANGE DATA FIELD
/DEPOSIT WORD
/CHECK BACK

```

```

5326 0000 /BANK SET
5327 7300 /CALL: LOCSET; BANK; REGB HAS CONSTANT
5330 1726 /
5331 7006 LOCSET, 0000
5332 7004 CLA CLL
5333 0121 TAD I LOCSET
5334 1101 RTL
5335 3342 RAL PMASK
5336 2326 AND KCDF
5337 3147 TAD EXCSET
5340 6201 DCA LOCSET
5341 1146 ISZ REGC
5342 0000 DCA REGC
5343 3547 TAD REGB
5344 2147 EXCSET, 0000
5345 5340 DCA I REGC
5346 6201 ISZ REGC
5347 5726 JMP PICSET
          CDF 00
          JMP I LOCSET
          /CLEAR AC
          /GET BANK
          /JUSTIFY
          /SOME MORE
          /BITS 06-08
          /ADD CDF
          /STORE FOR EXECUTION
          /INCREMENT RETURN
          /ZERO REGC
          /RESET DATA FIELD
          /GET CONSTANT
          /EXECUTE CDF
          /DEPOSIT C(REGB) IN BANK (N)
          /DONE?
          /NO, NEXT WORD
          /RESET DATA FIELD
          /RETURN

```

```

5400      *5400
5401      /PMODE-LMODE
5402      /INTERRUPT TEST: DO WE HAVE A SPURIOUS INTERRUPT ON-LINE?
5403      /
5404      INTTST, 0000
5405      CLA CLL CMA
5406      DCA REGA
5407      TSF
5408      JMS I SETFLG
5409      LINC
5410      LDA 20
5411      0020
5412      0004
5413      PDP
5414      JMP ,+1
5415      JMP ,+1
5416      CLA CLL
5417      TAD PNTP
5418      DCA RETURN
5419      ION
5420      NOP
5421      IOF
5422      JMS I NERRR
5423      JMS I ERROR
5424      INTSTM
5425      HLT
5426      SKP
5427      INTTST+1
5428      JMP I INTTST
5429
5430      LOCP,
5431      /FLAG SET?
5432      /NOT UP; GO SET IT
5433      /TO LMODE
5434      /GET BIT 07
5435      /I/O PRESET
5436      /ESF
5437      /TO PMODE
5438      /CLEAR INHIBIT
5439      /CLEAR INHIBIT
5440      /ZERO AC, LINK
5441      /GET POINTER
5442      /SET UP RETURN
5443      /ENABLE INTERRUPTS
5444      /WAIT
5445      /DISABLE INTERRUPTS
5446      /NO INTERRUPT ON-LINE
5447      /SPURIOUS INTERRUPT?
5448      /MESSAGE POINTER
5449      /ERROR HALT
5450      /RETURN
5451      /ISZ LOOP; SCOPE LOOP
5452      /RETURN

```

```

5431 1000 /
5432 7300 /PMODE FIND BANK
5433 1066 /
5434 7041 GETNXT, 0000
5435 1120 CLA CLL
5436 7653 TAD BANK
5437 5243 CIA
5438 2120 TAD PBANK
5439 1120 SNA CLA
5440 2120 JMP
5441 1120 ISZ
5442 5631 TAD
5443 3120 JMP I
5444 5631 DCA
                    JMP I GETNXT

/
/LMODE FIND BANK
5445 3000 GETNXL, 0000
5446 7300 CLA CLL
5447 1066 TAD BANK
5448 7006 RTL
5449 1021 TAD K0003
5450 7041 CIA
5451 1113 TAD LBANK
5452 7653 SNA CLA
5453 5261 JMP
5454 2113 ISZ
5455 1113 TAD LBANK
5456 5645 JMP I
5457 1021 TAD K0003
5458 3113 DCA LBANK
5459 5645 JMP I GETNXL

/
/RING THE BELL
5460 1000 BELLS, 0000
5461 7404 OSR
5462 7031 AND K0100
5463 764 SZA CLA
5464 5273 JMP
5465 1033 TAD K0207
5466 4574 JMS I
5467 5674 JMP I
5468 213 TST01

/CLEAR AC
/GET BANK
/2'S COMPLEMENT
/CHECK
/EQUAL?
/YES, RESET
/INCREMENT
/FETCH IT
/RETURN
/CLEAR BANK

/CLEAR AC
/FETCH AVAILABLE BANK
/JUSTIFY
/INCREASE TO MAXIMUM
/2'S COMPLEMENT
/COMPARE
/EQUAL?
/YES, RESET
/INCREMENT
/FETCH IT
/RETURN
/DON'T USE FIELDS 0-3
/SAVE IT
/RETURN

/READ SWITCHES
/SAVE SR05
/IS IT SET?
/YES, INHIBIT BELL
/GET BELL
/GO RING IT
/RETURN
/AVOID CLORBERING PASS COUNTER

```


/ERROR MESSAGES

/TST01
/CDF OR RDF FAILED (PMODE)
/SENT RCVD

5475 0024
5476 2324
5477 0061
5500 0003
5501 0406
5502 4017
5503 2240
5504 2204
5505 0640
5506 0601
5507 1114
5510 0504
5511 4050
5512 2015
5513 1704
5514 0551
5515 0023
5516 0516
5517 2440
5520 2203
5521 2604
5522 4000
5523 7777
5524 0146
5525 0147
5526 0000

/TST02

/CDF OR RDF FAILED (PMODE)
/SENT RCVD

5527 0024
5530 2324
5531 0062
5532 0003
5533 0416
5534 4017
5535 2240
5536 2204
5537 0640
5540 0601
5541 1114
5542 0504
5543 4050
5544 2015
5545 1704
5546 0551
5547 0023
5550 0516
5551 2440
5552 2203
5553 2604
5554 4000
5555 7777
5556 0146
5557 0147

```
5560 0000 EXIT
5561 0024 TST03M, 0024 /TST03
5562 2324
5563 6063
5564 0014 /LDF OR RDF FAILED (LMODE)
5565 0406 /SENT RCVD
5566 4017
5567 2240
5570 2204
5571 0640
5572 0601
5573 1114
5574 0504
5575 4050
5576 1415
5577 1704
5600 0551
5601 4000
5602 2305
5603 1624
5604 4022
5605 0326
5606 0400
5607 7777 EXITA
5610 0146 REG8
5611 0147 REGC
5612 0000 EXIT

5613 0024 TST04M, 0024 /TST04
5614 2324 /LDF OR RDF FAILED (LMODE)
5615 6064 /SENT RCVD
5616 0014
5617 0406
5620 4017
5621 2240
5622 2204
5623 0640
5624 0601
5625 1114
5626 0504
5627 4050
5630 1415
5631 1704
5632 0551
5633 0023
5634 0516
5635 2440
5636 2223
5637 2604
5640 4000
5641 7777 EXITA
5642 140 REG8
5643 0147 REGC
5644 0000 EXIT
```

5645 /TST25
5646 /CDF OR RDF FAILED (PMODE)
5647 /SENT RCVD
5648
5649
5650
5651
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5674
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5676

TST05M, 0024
2324
6065
0003
0406
4017
2240
2204
0640
0601
1114
0504
4050
2015
1704
0551
0023
0516
2440
2203
2604
4000
EXITA
REGH
REGC
EXIT

5677 /TST26
5678 /LDF OR RDF FAILED (LMODE)
5679 /SENT RCVD
5680
5681
5682
5683
5684
5685
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5687
5688
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5690
5691
5692
5693
5694
5695
5696
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5698
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5700
5701
5702
5703
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5708
5709
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5721
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5723
5724
5725
5726
5727
5728

TST06M, 0024
2324
6066
0014
0406
4017
2240
2204
0640
0601
1114
0504
4250
1415
1704
0551
0023
0516
2440
2203
2604
4000
EXITA
REGH
REGC
EXIT

```

5731 0024 TST07M, 0024 /TST07
5732 2324 2324
5733 6067 6067
5734 0020 0020 /PMODE INTERRUPT FAILED
5735 1517 1517
5736 0405 0405
5737 4011 4011
5740 1624 1624
5741 0522 0522
5742 2225 2225
5743 2024 2024
5744 4006 4006
5745 0111 0111
5746 1405 1405
5747 0400 0400
5750 7777 EXITA
5751 0000 EXIT

5752 0024 TST08M, 0024 /TST08
5753 2324 2324
5754 6070 6070
5755 0020 0020 /PMODE LOAD SF OR RIB FAILED
5756 1517 1517 / DF SF
5757 0405 0405
5760 4014 4014
5761 1701 1701
5762 0440 0440
5763 2306 2306
5764 4017 4017
5765 2240 2240
5766 2211 2211
5767 0240 0240
5770 0601 0601
5771 1114 1114
5772 0504 0504
5773 0040 0040
5774 0406 0406
5775 4040 4040
5776 4023 4023
5777 0600 0600
6000 7777 EXITA
6201 146 REG8
6002 147 REGC
6003 0000 EXIT

5778 0024 TST9AM, 0024 /TST9A
5779 0524 0524
5780 7101 7101
5781 0014 0014 /LMODE INTERRUPT FAILED
5782 1517 1517
5783 0405 0405
5784 4011 4011
5785 1624 1624
5786 0522 0522
5787 2225 2225

```

6016 2024
6017 4006
6020 0111
6021 1405
6022 0400
6023 7777
6024 0000
EXIT
EXIT

6025 0024
6026 2324
6027 6071
6030 0014
6031 1517
6032 0405
6033 4014
6034 1701
6035 0440
6036 2306
6037 4017
6040 2240
6041 2211
6042 0240
6043 0601
6044 1114
6045 0504
6046 0040
6047 0406
6050 4040
6051 4023
6052 0600
6053 7777
6054 0146
6055 0147
6056 0000

6057 0024
6060 2324
6061 6160
6062 0020
6063 1517
6064 0405
6065 4004
6066 0640
6067 0601
6070 1114
6071 0504
6072 4024
6073 1740
6074 3205
6075 2217
6076 4017
6077 1640
6100 0116
6101 4011
6102 1624

6078 0024
6081 2324
6082 6160
6083 0020
6084 1517
6085 0405
6086 4004
6087 0640
6088 0601
6089 1114
6090 0504
6091 4024
6092 1740
6093 3205
6094 2217
6095 4017
6096 1640
6097 0116
6098 4011
6099 1624

6099 0024
6102 2324
6103 6160
6104 0020
6105 1517
6106 0405
6107 4004
6108 0640
6109 0601
6110 1114
6111 0504
6112 4024
6113 1740
6114 3205
6115 2217
6116 4017
6117 1640
6118 0116
6119 4011
6120 1624

6121 0024
6124 2324
6125 6160
6126 0020
6127 1517
6128 0405
6129 4004
6130 0640
6131 0601
6132 1114
6133 0504
6134 4024
6135 1740
6136 3205
6137 2217
6138 4017
6139 1640
6140 0116
6141 4011
6142 1624

6143 0024
6146 2324
6147 6160
6148 0020
6149 1517
6150 0405
6151 4004
6152 0640
6153 0601
6154 1114
6155 0504
6156 4024
6157 1740
6158 3205
6159 2217
6160 4017
6161 1640
6162 0116
6163 4011
6164 1624

6165 0024
6168 2324
6169 6160
6170 0020
6171 1517
6172 0405
6173 4004
6174 0640
6175 0601
6176 1114
6177 0504
6178 4024
6179 1740
6180 3205
6181 2217
6182 4017
6183 1640
6184 0116
6185 4011
6186 1624

6187 0024
6190 2324
6191 6160
6192 0020
6193 1517
6194 0405
6195 4004
6196 0640
6197 0601
6198 1114
6199 0504
6200 4024
6201 1740
6202 3205
6203 2217
6204 4017
6205 1640
6206 0116
6207 4011
6208 1624

/TST09
/LMODE LOAD SF OR RIB FAILED
/ DF SF

/TST10
/PMODE DF FAILED TO ZERO ON AN INTERRUPT
/SENT SF RCVD

6103 0522
6104 2225
6105 2024
6106 0023
6107 0516
6110 2440
6111 4023
6112 0640
6113 4022
6114 0326
6115 0400
6116 7777
6117 0146
6120 0147
6121 0151
6122 0000

TST11M, 2024 /TST11

6123 0024
6124 2324
6125 6161
6126 0014
6127 1517
6130 0405
6131 4004
6132 0640
6133 0601
6134 1114
6135 0504
6136 4024
6137 1740
6140 3205
6141 2217
6142 4017
6143 1640
6144 0116
6145 4011
6146 1624
6147 0522
6150 2225
6151 2024
6152 0023
6153 0516
6154 2440
6155 4023
6156 0640
6157 4022
6160 3206
6161 0400
6162 7777
6163 0146
6164 0147
6165 0151
6166 0000

/LMODE DF FAILED TO ZERO ON AN INTERRUPT
/SENT SF RCVD

TST12M, 2024 /TST12

/DCA I - TAD I FAILED
/FIELD LOCN SENT RCVD

2324
6170 2324
6171 6162
6172 0004
6173 0301
6174 4011
6175 4055
6176 4024
6177 0104
6200 4011
6201 4006
6202 0111
6203 1405
6204 0400
6205 0611
6206 1404
6207 4014
6210 1703
6211 1640
6212 2305
6213 1624
6214 4022
6215 0326
6216 0400
6217 7777
6220 0146
6221 0145
6222 0076
6223 0147
6224 0000

/TST13

TST13M, 0024

6225 0024
6226 2324
6227 6163
6230 0023
6231 2401
6232 4055
6233 4014
6234 0401
6235 4006
6236 0111
6237 1405
6240 0400
6241 0201
6242 1613
6243 4014
6244 1703
6245 1640
6246 2305
6247 1624
6250 4022
6251 0326
6252 0400
6253 7777
6254 0146
6255 0145

/STA - LDA FAILED
/BANK LOCN SENT RCVD

6256	0076	K5252
6257	0147	REGC
6260	0000	EXIT
6261	0024	TST14M, 0024
6262	2324	2324
6263	6164	6164
6264	0014	0014
6265	1517	1517
6266	0405	0405
6267	4012	4012
6270	2515	2515
6271	2040	2040
6272	2301	2301
6273	2605	2605
6274	4022	4022
6275	0524	0524
6276	2522	2522
6277	1640	1640
6300	0601	0601
6301	1114	1114
6302	0504	0504
6303	4006	4006
6304	1722	1722
6325	4016	4016
6306	1722	1722
6307	1501	1501
6310	1440	1440
6311	1225	1225
6312	1520	1520
6313	4000	4000
6314	7777	EXITA
6315	0000	EXIT
6316	0024	TST15M, 0024
6317	2324	2324
6320	6165	6165
6321	0004	0004
6322	1222	1222
6323	4006	4006
6324	0111	0111
6325	1405	1405
6326	0440	0440
6327	2417	2417
6330	4011	4011
6331	1610	1610
6332	1102	1102
6333	1124	1124
6334	4012	4012
6335	2515	2515
6336	2040	2040
6337	2301	2301
6340	2605	2605
6341	4000	4000
6342	7777	EXITA

/TST14

/LMODE JUMP SAVE RETURN FAILED FOR NORMAL JUMP

/TST15

/DJR FAILED TO INHIBIT JUMP SAVE

6343	0000	EXIT
6344	0024	TST16M, 0024
6345	2324	2324
6346	6166	6166
6347	0014	0014
6350	1517	1517
6351	0405	0405
6352	4012	4012
6353	1520	1520
6354	4006	4006
6355	0111	0111
6356	1405	1405
6357	0440	0440
6360	2417	2417
6361	4003	4003
6362	1405	1405
6363	0122	0122
6364	4004	4004
6365	1222	1222
6366	4000	4000
6367	7777	EXITA
6370	0000	EXIT
6371	0024	TST17M, 0024
6372	2324	2324
6373	6167	6167
6374	0020	0020
6375	1517	1517
6376	1405	1405
6377	4012	4012
6400	2515	2515
6421	2040	2040
6402	0114	0114
6403	2405	2405
6404	2205	2205
6425	0440	0440
6406	0305	0305
6407	1414	1414
6410	4060	4060
6411	6060	6060
6412	6000	6000
6413	7777	EXITA
6414	0000	EXIT
6415	0024	TST18M, 0024
6416	2324	2324
6417	6170	6170
6420	0020	0020
6421	1517	1517
6422	1405	1405
6423	4011	4011
6424	1706	1706
6425	4001	4001
6426	1424	1424

/TST16

/LMODE JUMP FAILED TO CLEAR DJR

/TST17

/PMODE JUMP ALTERED CELL 0000

/TST18

/PMODE IOF ALTERED CELL 0000

```

6427 0522
6430 0504
6431 4003
6432 0514
6433 1440
6434 6060
6435 6060
6436 4000
6437 7777
6440 0000

```

```

TST19M, 0024 /TST19
2324
6171
0014 /LMODE IOF ALTERED CELL 0000
1517
0405
4011
1706
4001
1424
0522
0504
4003
0514
1440
6060
6060
4000
EXITA
EXIT

```

```

TST21M, 0024 /TST20
2324
6260
0020 /PMODE JUMP CLEARED DJR
1517
2405
4012
2515
2240
0314
0501
2205
0440
0412
2200
EXITA
EXIT

```

```

TST21M, 0024 /TST21
2324
6261
0004 /DJR INHIBITED PMODE INTERRUPT SAVE
1222

```

6513	4011	4011	
6514	1610	1610	
6515	1102	1102	
6516	1124	1124	
6517	0504	0504	
6520	4020	4020	
6521	1517	1517	
6522	0405	0405	
6523	4011	4011	
6524	1624	1624	
6525	0522	0522	
6526	2225	2225	
6527	2024	2024	
6530	4023	4023	
6531	0126	0126	
6532	0500	0500	
6533	7777	EXITA	
6534	0000	EXIT	
TST22M, 0024			
6535	0024	0024	/TST22
6536	2324	2324	/NON-EXISTANT MEMORY READ-BACK FAILED
6537	6262	6262	/BANK DATA
6540	0016	0016	/REGD REGC
6541	1716	1716	
6542	5505	5505	
6543	3011	3011	
6544	2324	2324	
6545	0116	0116	
6546	2440	2440	
6547	1505	1505	
6550	1517	1517	
6551	2231	2231	
6552	4022	4022	
6553	0501	0501	
6554	0455	0455	
6555	0201	0201	
6556	0313	0313	
6557	4026	4026	
6562	0111	0111	
6561	1405	1405	
6562	0400	0400	
6563	0201	0201	
6564	1613	1613	
6565	4024	4024	
6566	0124	0124	
6567	0100	0100	
6570	7777	EXITA	
6571	151	REGD	
6572	147	REGC	
6573	0000	EXIT	
TST23M, 0000			
6574	0000	0000	/RESERVED
6575	0024	0024	/TST24
6576	2324	2324	
6577	6264	6264	

/CIF FAILED TO LOAD PROPER IF
/SENT RCVD

6600	P003	0003
6601	1106	1106
6602	4006	4006
6603	0111	0111
6604	1405	1405
6605	0440	0440
6606	2417	2417
6607	4040	4040
6608	1417	1417
6609	0104	0104
6610	4020	4020
6611	2217	2217
6612	2005	2005
6613	2240	2240
6614	1106	1106
6615	0023	0023
6616	0516	0516
6617	2440	2440
6618	2203	2203
6619	2604	2604
6620	4000	4000
6621	7777	7777
6622	0146	0146
6623	0147	0147
6624	0000	0000
6625	REG8	REG8
6626	REGC	REGC
6627	EXIT	EXIT
6628	EXITA	EXITA

1TST25

TST25M, 0024

/LIF FAILED TO LOAD PROPER IF
/SENT RCVD

0200	0014	1106	4006	0111	1405	0440	2417	4014	1701	0440	2022	1720	0522	4011	0600	2325	1624	4022	0326	1422	EXIT	REGD	REGC	EXIT
0200	0014	1106	4006	0111	1405	0440	2417	4014	1701	0440	2022	1720	0522	4011	0600	2325	1624	4022	0326	1422	EXIT	REGD	REGC	EXIT

1TST26

TST26M, 2224

6665 2324
6666 6266
6667 0003
6670 1106
6671 4006
6672 0111
6673 1405
6674 0440
6675 2417
6676 4006
6677 1116
6700 0440
6701 2022
6702 1720
6703 0522
6704 4015
6705 0515
6706 1722
6707 3100
6710 2305
6711 1624
6712 4022
6713 0326
6714 0400
6715 7777
6716 1146
6717 1147
6720 0000

/CIF FAILED TO FIND PROPER MEMORY
/SENT RCVD

/TST27
/PMODE INTERRUPTS NOT INHIBITED BY CIF
/BANK

TST27M, 0024
6721 0024
6722 2324
6723 6267
6724 0020
6725 1517
6726 0405
6727 4011
6730 1624
6731 0522
6732 2225
6733 2024
6734 2340
6735 1617
6736 2440
6737 1116
6740 1011
6741 0211
6742 2405
6743 0440
6744 0231
6745 4003
6746 1106
6747 4000
6750 0201
6751 1613
6752 4000

6753 7777 EXITA
6754 1146 REG8
6755 0000 EXIT

TST28M, 0024
2324 2324
6270 6270
0014 0014
1517 1517
0405 0405
4014 4014
1106 1106
4006 4006
0111 0111
1405 1405
0440 0440
2417 2417
4011 4011
1610 1610
1102 1102
1124 1124
4011 4011
1624 1624
0522 0522
2225 2225
2024 2024
2500 2500
0201 0201
1613 1613
4000 4000
EXITA
REG8
EXIT

/TST28

/LMODE LIF FAILED TO INHIBIT INTERRUPTS
/BANK

/TST29
/LMODE JMP 0 FAILED TO CLEAR
/INTERRUPT INHIBIT

TST29M, 0024
2324 2324
6271 6271
0014 0014
1517 1517
0405 0405
4012 4012
1520 1520
4060 4060
4006 4006
0111 0111
1405 1405
0440 0440
2417 2417
4003 4003
1405 1405
0122 0122
4011 4011
1624 1624
0522 0522
2225 2225

7040 2024
7041 4011
7042 1610
7043 1102
7044 1124
7045 4000
7046 0201
7047 1613
7050 4000
7051 7777
7052 1146
7053 0000

TST30M, 0024
/TST30
/LMODE DUE-JMP 0 FAILED
/TO LOAD IF
/BANK

7054 0024
7055 2324
7056 6360
7057 0014
7060 1517
7061 0405
7062 4004
7063 1222
7064 5512
7065 1520
7066 4060
7067 4006
7070 0111
7071 1405
7072 0440
7073 2417
7074 4014
7075 1701
7076 0440
7077 1106
7080 0002
7081 0116
7082 1300
7083 7777
7084 1146
7085 0000

/TST32
/LMODE ION-LIF FAILED TO
/INHIBIT INTERRUPTS

TST32M, 0024
7086 0024
7087 2324
7088 6362
7089 0014
7090 1517
7091 1405
7094 4011
7095 1716
7096 5514
7097 1106
7098 4006
7099 0111
7100 1405
7101 0440

7124 2417
 7125 4011
 7126 1610
 7127 1102
 7130 1124
 7131 4011
 7132 1624
 7133 0522
 7134 2225
 7135 2024
 7136 2300
 7137 EXIT

TST33M, 0024
 7140 2024
 7141 2324
 7142 6363
 7143 0014
 7144 1517
 7145 0405
 7146 4014
 7147 1106
 7150 5512
 7151 1520
 7152 4016
 7153 4006
 7154 0111
 7155 1405
 7156 1440
 7157 2417
 7160 4014
 7161 1701
 7162 0440
 7163 2306
 7164 0040
 7165 1106
 7166 4040
 7167 4004
 7170 0640
 7171 4040
 7172 2306
 7173 4000
 7174 7777
 7175 1147
 7176 1152
 7177 0151
 7178 0000

/TST33
 /LMODE LIF-JMP N FAILED TO LOAD SF
 / IF DF SF

TST34M, 0024
 7201 1024
 7202 2324
 7203 6364
 7204 0014
 7205 1517
 7206 0405
 7207 4022
 7208 1506

/TST34
 /LMODE RMF IN EXTENDED
 /BANK FAILED

EXITA
 REGC
 REGD
 REGD
 EXIT

7211 4011
7212 1640
7213 0530
7214 2405
7215 1604
7216 0504
7217 4002
7220 0116
7221 1340
7222 0601
7223 1114
7224 0504
7225 0002
7226 0116
7227 1340
7230 4023
7231 0600
7232 7777
7233 0146
7234 0147
7235 0000
4011
1640
0530
2405
1604
0504
4002
0116
1340
0601
1114
0504
0002
0116
1340
4023
0600
EXITA
REG0
REGC
EXIT

/TST35
/PMODE AUTO-INDEX FAILED
/BANK CELL ADDR

TST35M, 0024
7236 0024
7237 2324
7240 6365
7241 0020
7242 1517
7243 0405
7244 4001
7245 2524
7246 1755
7247 1116
7250 0405
7251 3040
7252 1601
7253 1114
7254 0504
7255 0002
7256 0116
7257 1340
7260 0305
7261 1414
7262 4001
7263 0404
7264 2200
7265 7777
7266 0146
7267 0147
7270 0151
7271 0000
0024
2324
6365
0020
1517
0405
4001
2524
1755
1116
0405
3040
1601
1114
0504
0002
0116
1340
0305
1414
4001
0404
2200
EXITA
REG0
REGC
REGD
EXIT

/TST36
/PMODE AUTO-INDEX FAILED
/FIELD LOCN

TST36M, 0024
7272 0024
7273 2324
7274 6366
7275 4014
0024
2324
6366
4014

7276	1517	1517	
7277	0405	0405	
7300	4001	4001	
7301	2524	2524	
7302	1755	1755	
7303	1116	1116	
7304	0405	0405	
7305	3040	3040	
7306	0601	0601	
7307	1114	1114	
7310	0504	0504	
7311	0006	0006	
7312	1105	1125	
7313	1404	1404	
7314	4014	4014	
7315	1703	1703	
7316	1600	1600	
7317	7777	EXITA	
7320	0146	REGB	
7321	0147	REGC	
7322	0000	EXIT	
7323	0005	0005	
7324	3024	3024	
7325	4015	4015	
7326	0515	0515	
7327	4024	4024	
7330	2324	2324	
7331	4020	4020	
7332	0123	0123	
7333	2355	2355	
7334	5555	5555	
7335	7777	EXITA	
7336	0017	COUNT	
7337	4444	EXITB	
7340	0023	INTSM,	2223
7341	2025		2025
7342	2211		2211
7343	1725		1725
7344	2340		2340
7345	1116		1116
7346	2405		2405
7347	2222		2222
7350	2520		2520
7351	2441		2441
7352	0005		0005
7353	0131		0131
7354	0503		0503
7355	1340		1340
7356	1117		1117
7357	1340		1340
7360	1157		1157
7361	1740		1740
7362	2022		2022
7363	523		523

/EXT MEM TST PASS--(PASS)

/SPECIAL RESTART: EVENTUALLY GETS TO TST01

/SPURIOUS INTERRUPT:

/(CHECK IOC I/O PRESET)

/PDP-11 ATTENDED MEMORY TEST, VERSION 2;

MAINDEC 12-D1AC-L

PAL10

V141

8-OCT-70

10:32

PAGE 57-13

7364 W524
7365 5100
7366 0000

0524
5100
EXIT

5

2000	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
2100	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
2200	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
2300	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
2400	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
2500	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
2600	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
2700	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
1000	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
1100	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
1200	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
1300	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
1400	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
1500	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
1600	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
1700	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
2000	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
2100	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
2200	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
2300	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
2400	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
2500	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
2600	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
2700	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111

3200
3100
3200
3300
3400
3500
3600
3700

[illegible]

5000	11111111	12111111	11111111	11111111	11111111
5100	11111111	12111111	11111111	11111111	11111111
5200	11111111	12111111	11111111	11111111	11111111
5300	11111111	12111111	11111111	11111111	11111111
5400	11111111	12111111	11111111	11111111	11111111
5500	11111111	12111111	11111111	11111111	11111111
5600	11111111	12111111	11111111	11111111	11111111
5700	11111111	12111111	11111111	11111111	11111111

[illegible]

7280	11111111	11111111	11111111	11111111	11111111
7281	11111111	11111111	11111111	11111111	11111111
7282	11111111	11111111	11111111	11111111	11111111
7283	11111111	11111111	11111111	11111111	11111111
7284	11111111	11111111	11111111	11111111	11111111
7285	11111111	11111111	11111111	11111111	11111111
7286	11111111	11111111	11111111	11111111	11111111
7287	11111111	11111111	11111111	11111111	11111111
7288	11111111	11111111	11111111	11111111	11111111
7289	11111111	11111111	11111111	11111111	11111111
7290	11111111	11111111	11111111	11111111	11111111
7291	11111111	11111111	11111111	11111111	11111111
7292	11111111	11111111	11111111	11111111	11111111
7293	11111111	11111111	11111111	11111111	11111111
7294	11111111	11111111	11111111	11111111	11111111
7295	11111111	11111111	11111111	11111111	11111111
7296	11111111	11111111	11111111	11111111	11111111
7297	11111111	11111111	11111111	11111111	11111111
7298	11111111	11111111	11111111	11111111	11111111
7299	11111111	11111111	11111111	11111111	11111111

ADD	2000	EXCSET	5342	LDA	1000	PAL10	V141	8-OCT-70	PMAK	0121
ASCII	5050	EXDF33	0072	LDF	2640				PNT30	2007
ASCRXT	5025	EXIF33	0073	LEND	4205				PNT30N	0141
AUTO11	5015	EXIT	0000	LHAN	0042				PNTA	0122
AUTO12	0016	EXITA	7777	LIF	0600				PNTB	0123
AZE	0450	EXITB	4444	LINC	6141				PNTC	0124
BAK22	1300	FAL22	1310	LINTR	0040				PNTCA	0125
BANK	0066	FAL35	2522	LJMP	6000				PNTD	0126
BCL	1540	FLAG	5253	LMASK	0114				PNTE	0127
BELL	0067	GETBNK	0074	LNOP	0016				PNTF	0130
BELLS	5464	GETBNL	0075	LOCA	0502				PNTG	0131
BNKSET	0070	GETNXL	5445	LOCB	0527				PNTH	0132
BSE	1620	GETNXT	5431	LOCC	0620				PNTI	0133
CLR	0011	GOAUTO	2636	LOCCA	0564				PNTJ	0134
COUNT	0017	HERE	5136	LOCD	0660				PNTK	0135
CRLF	5153	INCREL	5313	LOCE	0722				PNTL	0136
DATUM	5114	INTSIM	7340	LOCF	1246				PNTD	0137
DATYP	5162	INTTST	5400	LOGG	1430				PNTP	0140
DEST36	2633	IOB	0500	LOCH	1473				PONT	4204
DJR	0006	K0003	0021	LOCI	1605				PPOINT	0142
END	2534	K0007	0022	LOCJ	1651				PREG	0143
ERL1	4172	K0010	0023	LOCK	1734				RANDOM	0144
ERL10	4156	K0017	0024	LOCL	2012				RANDY	5200
ERL11	4155	K0020	0025	LOCO	4222				READ0	1316
ERL12	4154	K0040	0026	LOCP	5423				READ1	1342
ERL13	4153	K0070	0027	LOCSET	5326				REDO	5137
ERL14	4152	K0077	0030	LOK	2657				REGA	0145
ERL15	4151	K0100	0031	LPPOINT	0065				REG8	0146
ERL16	4150	K0177	0032	LREG	0064				REGC	0147
ERL17	4147	K0207	0033	LREG1	0013				REGCN	0150
ERL2	4164	K0212	5175	LSET	0050				REGD	0151
ERL3	4163	K0215	5174	LSKP	0456				REGI	0152
ERL36	2661	K0400	0034	LSERR	0115				RELOC	5261
ERL4	4162	K1026	0035	M40	5177				RELOC8	0153
ERL5	4161	K1777	0036	M4444	5173				RELNT	0154
ERL6	4160	K2000	0037	MSTART	0177				RETURN	0155
ERL7	4157	K240	5176	NBNK	2611				RNA	5240
ERR10	2505	K5252	0076	NERROR	0116				RNB	5241
ERR11	2504	K6220	0077	NERRCS	5000				RNC	5242
ERR12	2503	K7774	0100	NOW1	0374				RND	5243
ERR13	2512	KCDF	0101	NOW2	0452				ROL	0240
ERR14	2501	KCIF	0102	OCTYP	5132				ROR	0300
ERR15	2500	KHLT	0103	OK35	2516				SET	0060
ERR16	2477	KIOB	0105	PASPNT	4223				SETFLG	0156
ERR17	2476	KLOF	0106	PASS	4206				SPACE	0157
ERROR	0071	KLOF	0104	PASSN	0117				SHO	1500
ERRORS	5020	KLIF	0111	PBANK	0120				STA	1040
EX20	1725	KLJMP	0112	PDP	0002				START	0200
EXAUT	2653	KLVOP	0107	PICKUP	5321				SIC	4000
EXC12	1763	KRIF	0110	PICSET	5340				TST01	0213
EXC13	1722	LAUTO	4222	PINT	0014				TST01M	5475
EXCREL	5323	LBANK	0113	PINT8	0000				TST02	2240

TST02M	5527	TST25M	6631
TST03	0265	TST26	1514
TST03M	5361	TST26M	6664
TST04	0315	TST27	1556
TST04M	5613	TST27M	6721
TST05	0345	TST27N	0165
TST05M	5645	TST28	1613
TST06	0413	TST28M	6756
TST06M	5677	TST28N	0166
TST07	0473	TST29	1663
TST07M	5731	TST29M	7013
TST08	0512	TST30	1747
TST08M	5752	TST30M	7054
TST09	0576	TST30N	0167
TST09M	6025	TST30X	1763
TST10	0643	TST32	2033
TST10M	6057	TST32M	7106
TST11	0701	TST32N	0170
TST11M	6123	TST32X	2043
TST12	0746	TST33	2072
TST12A	0755	TST33M	7140
TST12M	6167	TST33N	0171
TST12N	0161	TST34	2152
TST13	1004	TST34M	7201
TST13A	1016	TST34N	0172
TST13M	6225	TST35	2400
TST13N	0162	TST35M	7236
TST14	1044	TST35N	0173
TST14M	6261	TST36	2600
TST15	1064	TST36M	7272
TST15M	6316	TST36X	2530
TST16	1105	TST37M	7323
TST16M	6344	TST9A	0550
TST17	1127	TST9AM	6004
TST17M	6371	TSTINT	0160
TST18	1145	TYPE	0174
TST18M	6415	TYPECH	5077
TST19	1163	TYP0UT	5244
TST19M	6441	XDF33	2120
TST20	1204	XIF33	2121
TST20M	6465	XSK	0220
TST21	1231		
TST21M	6506		
TST22	1262		
TST22M	6535		
TST23	137		
TST23M	6574		
TST23N	0163		
TST24	1413		
TST24M	6575		
TST24N	0164		
TST25	1446		

ERRORS DETECTED: 0

LINKS GENERATED: 0

RUN-TIME: 25 SECONDS

3K CORE USED