RELAYTST

IDENTIFICATION

Product Code:

Maindec 12-D8AB-D(P)

Product Name:

PDP-12 Relay Register Test

Date Created:

August 1, 1969

Maintainer:

Diagnostics Group

Author:

J. Kelly

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ABSTRACT

The relay register diagnostic and exerciser consists of three (3) separate programs. The first, and major, program thoroughly diagnoses and exercises the relay flip flop register on a bit-by-bit basis. The second routine is a three instruction loop which allows the service engineer to transfer the contents of the right switches RSW bits 6 through 11 into the relay register. This is useful for signal tracing and setting specific number patterns into the relays. The third and last routine alternately sets and clears the entire relay register at a 100 millisecond rate, as determined by the teletype logic. This program allows the service engineer to examine the electromechanical characteristics of the actual relays themselves.

2. REQUIREMENTS

2.1 Equipment

- a. A standard basic PDP-12 computer
- b. A DR-12 relay register option
- c. ASR-33 teletype
- d. An oscilloscope (needed only if you wish to examine the relay characteristics.)

2.3 Preliminary Programs

All basic PDP-12 instruction diagnostic programs must have been successfully run prior to attempting to test the relay register.

3. LOADING PROCEDURES

3.1 Method

This program must be loaded with the binary loader. If you are unfamiliar with the proper binary loading procedures refer to "Appendix A" of this program, otherwise procede with the following:

- a. Set the teletype reader switch to FREE.
- b. Open the teletype reader and insert the program tape so that the arrows on the tape are visible to, and pointing toward the operator.
- c. Close the reader and set the reader switch to START.
- d. Set the teletype front panel switch to ON LINE.
- e. Set the LEFT switch to 7777.
- f. Set the RIGHT switch to 4000.
- g. Set the MODE switch to 8 mode.
- h. Depress I/O preset.
- i. Depress START LS.
- . When the program tape has been read in the computer will halt.
- k. The ACCUMULATOR must be = 0000, if it is not, a read in error has occurred and one might try reloading the binary loader.
- 1. Remove the program tape from the reader.

4. STARTING PROCEDURE

4.1 Starting Address "RELAY FLIP FLOP REGISTER TEST"

The major diagnostic and exerciser "Relay Flip Flop Register Test" starts at address 20 to run this test procede as follows:

- a. Set the MODE switch to 8 mode.
- b. Set IF = 0, DF = 0.
- c. Depress I/O preset.
- d. Depress START 20.
- e. The LSW and RSW have no effect on this test.

The state of all other switches, i.e. LEFT, RIGHT and SENSE switches, have no effect on this routine. This test, once started will run continuously with no halts. If it should halt, indicating an error, consult section 5 of this write-up along with the program listing. This test must be allowed to run for at least 2 minutes.

"RSW TO RELAY TEST"

The second test "RSW To Relay Test" starts at address 1000 and once running transfers the contents of RSW bits 6 through 11 into the relay register. This program does not perform any error checking and is intended solely as a visual display and signal tracing aide. To start it, as follows:

- a. Set the Mode switch to LINC.
- b. Depress I/O preset.
- c. Set 1000 into the LEFT switches.
- d. Depress START LS.
- e. Set RSW 0011.
- f. Set any combination of numbers into the right most 6 switches bits 6 through 11 in RSW and observe that the same data appears in both the RELAY REGISTER indicator lamps and the accumulator.
- g. It should be noted that RSW bits 0 through 5 also appear in the AC but have no effect on the relay register.

"RELAY TEST"

The third and final test is designed to allow the service engineer to observe the relay switching action. By applying a small DC voltage through the relay contacts being rested thence to the oscilloscope, one may observe the make-break action of each relay contact. Normally, this test need not be run unless a specific relay problem is suspected. To run this test proceed as follows:

- a. Set the MODE switch to LINC mode.
- b. Depress I/O preset.
- c. Set LEFT switches to 1003.
- d. Depress START LS.

All relay indicaters along with the entire accumulator will alternately set and clear at a millisecond rate.

5. ERRORS

Any errors which occur while running the RELAY Flip-Flop register will cause the computer to halt at a predesignated address. This address which appears in the (MA) MEMORY ADDRESS register along with the data appearing in the ACCUMULATOR and the RELAY REGISTER indicators allows us to ascertain the nature of the failure. All errors are listed below:

C(MA)	C(AC)	C(RELAY)	EXPLANATION
0025	0000	00	Any bit set in either the AC or Relay register was not cleared by I/O preset.
0033	0000	00	RTA from a cleared relay register failed to clear the entire AC.
0041	7777	77	ATR modified the AC. The AC was set to 7777, ATR was issued, it should have left the AC as 7777. The state of the relay register is not checked at this time.
0047	0000	00	ATR modified the AC. The AC was set to 0000, ATR was issued; it should have left the AC as 0000. The state of the relay register was not checked at this time.
0056	0001	01	AC11 transfer to and from relay 5 failed.
0065	0002	02	AC10 transfer to and from relay 4 failed.
0074	0004	04	AC9 transfer to and from relay 3 failed.
0103	0010	10	AC8 transfer to and from relay 2 failed.
0112	0020	20	AC7 transfer to and from relay 1 failed.
0121	0040	40	AC6 transfer to and from relay 0 failed.

0130	0077	77	ATR RTA 77 failed.
0137	0076	76	ATR RTA 76 failed.
0146	0075	75	ATR RTA 75 failed.
0155	0073	73	ATR RTA 73 failed.
0164	0067	67	ATR RTA 67 failed.
0173	00 <i>5</i> 7	57	ATR RTA 57 failed.
0202	0037	37	ATR RTA 37 failed.
0260	0052	52	The number 52 was loaded into the relay register and read back 20 consecutive times before testing. The AC and relay register should be identical, if any differences occur that relay flop is bad.
0344	0025	25	Same as previous test.
0417	0077	77	The relay register was loaded with with 77, read back complemented Loaded again, etc., 12 times. The Accumulator and the relay register should both equal 77. Any differences indicate the failing bit.
0456	xxxx	XX	This test is a random number test wherein random numbers are loaded into and read out of the relay register. The AC bits 6 through 11 contain the data which was sent to the relay register. The relay register contains the actual data received.
0471	0077	00	The AC was distrubed while trying to load the relay register.
0475	0077	00	The relay register was disturbed. A test was performed on M115 L08 of the relay register gating. The test caused at least one of the inputs of the "AND" gate to be disqualified inhibiting the ATR command.

The relay register was loaded with 52, read back complemented loaded again, etc., 12 times. The accumulator and the relay register should both equal 52, any differences indicate the failing bit.

APPENDIX A

PDP-8 MODE PERFORATED - TAPE LOADER

READIN MODE LOADER

The readin mode (RIM) loader is a minimum length, basic, perforated-tape program for the 33 ASR. It is initially stored in memory by manual use of the operator console keys and switches. The loader is permanently stored in 18 locations of page 37.

The RIM loader can only be used in conjunction with the 33ASR reader (not the high-speed perforated-tape reader). Because a tape in RIM format is, in effect, twice as long as it need be, it is suggested that the RIM loader be used only to read the binary loader when using the 33 ASR. (NOTE: Some PDP-12 diagnostic program tapes are in RIM format).

The complete PDP-12 RIM loader (SA = 7756 is as follows:

Absolute Address	Octal Content	Tag	Instruction I Z	Comments
7756	6032	BEG,	ксс	/CLEAR AC AND FLAG
7757,	6031	,	KSF	SKIP IF FLAG = 1
7760	53.57		JMP -1	LOOKING FOR CHARACT
7761,	6036		KRB	READ BUFFER
7762,	7106		CLL RTL	,
7763,	7006		RTL	/CHANNEL 8 IN ACO
7764,	7510		SPA	CHECKING FOR LEADER
7765,	5357		JMP BEG +1	FOUND LEADER
7766,	7006		RTL	OK, CHANNEL 7 IN LINK
7767	6031		KSF	,
7770,	5367		JMP -1	
<i>777</i> 1,	6034		KRS	/READ, DO NOT CLEAR
7772,	7420		SNL	CHECKING FOR ADDRESS
7773,	3776		DCA I TEMP	STORE CONTENT
7774,	3376		DCA TEMP	STORE ADDRESS
7775,	53 56		JMP BEG	/NEXT WORD
7776,	0	TEMP,	0	TEMP STORAGE
7777,	5XXX	ŕ	JMP X	/JMP START OF BIN

Placing the RIM loader in core memory by way of the operator console keys and switches is accomplished as follows:

- a. Set the starting address 7756 in the LEFT switches.
- b. Set the first instruction (6032) in the RIGHT switches.
- c. Press the FILL switch.
- d. Set the next instruction (6031) in the RIGHT switches.
- e. Press the FILL STEP switch.
- f. Repeat steps d and e until all 16 instructions have been deposited.

To lead a tape in RIM format, place the tape in the reader, set the LEFT switches to the starting address 7756 of the RIM loader (not of the program being read), press the START LS key, and start the Teletype reader.

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