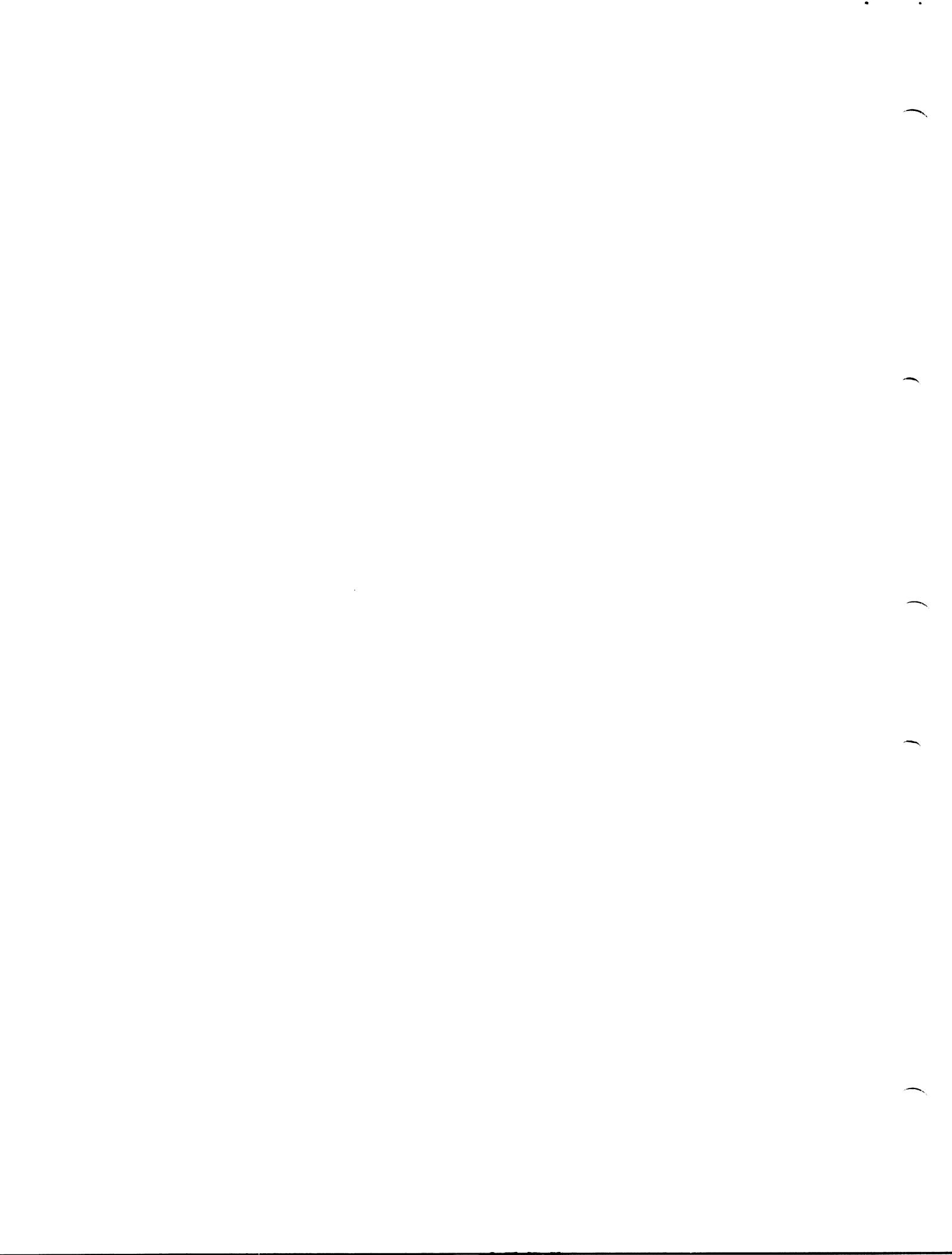


IDENTIFICATION

PRODUCT CODE: MAINDEC-8E-DØEA-D (D)
PRODUCT NAME: RANDOM TAD TEST
DATE CREATED: NOVEMBER 2, 1970
MAINTAINER: DIAGNOSTIC GROUP
AUTHOR: MICHAEL DAVIS

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1. ABSTRACT

THIS PROGRAM TESTS THE TAD INSTRUCTING OF THE PDP-8E. THE TAD INSTRUCTION, INSTRUCTION ADDRESS, OPERAND ADDRESS AND BOTH OPERANDS ARE PRODUCED BY RANDOM NUMBER GENERATORS.

2. REQUIREMENTS

2.1 EQUIPMENT

PDP-8E EQUIPPED WITH AT LEAST 4K OF MEMORY.

TELETYPE.

2.2 STORAGE

THE PROGRAM IS LOADED INTO LOCATIONS 6600 THRU 7577.
THE TEST AREA IS 5000-6577. TEMPORARY STORAGE LOCATIONS
ARE LOCATED ON PAGE 0.

2.3 PRELIMINARY PROGRAMS

MAINDEC-8E-D0AA, D0BA, D0CA, D0DA

3. LOADING PROCEDURE

THE STANDARD PROCEDURE FOR LOADING BINARY TAPES IS TO BE USED.

4. STARTING PROCEDURE

4.1 STARTING ADDRESS

0200

4.2 CONTROL SWITCH SETTINGS

SR00=1, SUPPRESS HALT ON ERROR
SR03=1, SUPPRESS END OF PASS TIMEOUT
SR09=1, HOLD DATA 1 CONSTANT
SR10=1, HOLD DATA 2 CONSTANT
SR11=1, HOLD INSTRUCTION CONSTANT

4.3 OPERATOR ACTION

4.3.1 SET SR TO L20C

4.3.2 PRESS LOAD ADDRESS SWITCH

4.3.3 SET SR TO 2200

4.3.4 PRESS CLEAR AND CONTINUE SWITCHES

5. OPERATING PROCEDURE

SAME AS 4.

ERRORS

6.1 ERROR HALT

IF THE RESULTS OF THE TAD INSTRUCTION ARE INCORRECT,
THAT IS, IF THE ACTUAL AND SIMULATED LINKS, OR THE ACTUAL
AND SIMULATED SUMS DO NOT AGREE, THE PROGRAM
WILL HALT AT 7407 WITH DATA1 IN THE AC.

DEPRESS CONTINUE TO DISPLAY DATA2 IN THE AC.
DEPRESS CONTINUE TO DISPLAY TAD INSTRUCTION IN AC.
DEPRESS CONTINUE TO DISPLAY INSTRUCTION ADDRESS IN AC.
DEPRESS CONTINUE TO DISPLAY DATA2 ADDRESS IN AC.
DEPRESS CONTINUE TO DISPLAY INDIRECT POINTER (USED BY INDIRECT
TAD) IN AC.
DEPRESS CONTINUE TO RESUME TEST

6.2 ERROR RECOVERY

SEE 6.1

SET SR00=1 TO PREVENT HALT AFTER ERROR,
SET SR03=1 TO SUPPRESS END OF PASS TIMEOUT,
SET SR09-SR11=1 TO HOLD INSTRUCTION AND DATA CONSTANT,

7. RESTRICTIONS

NONE

8. EXECUTION TIME

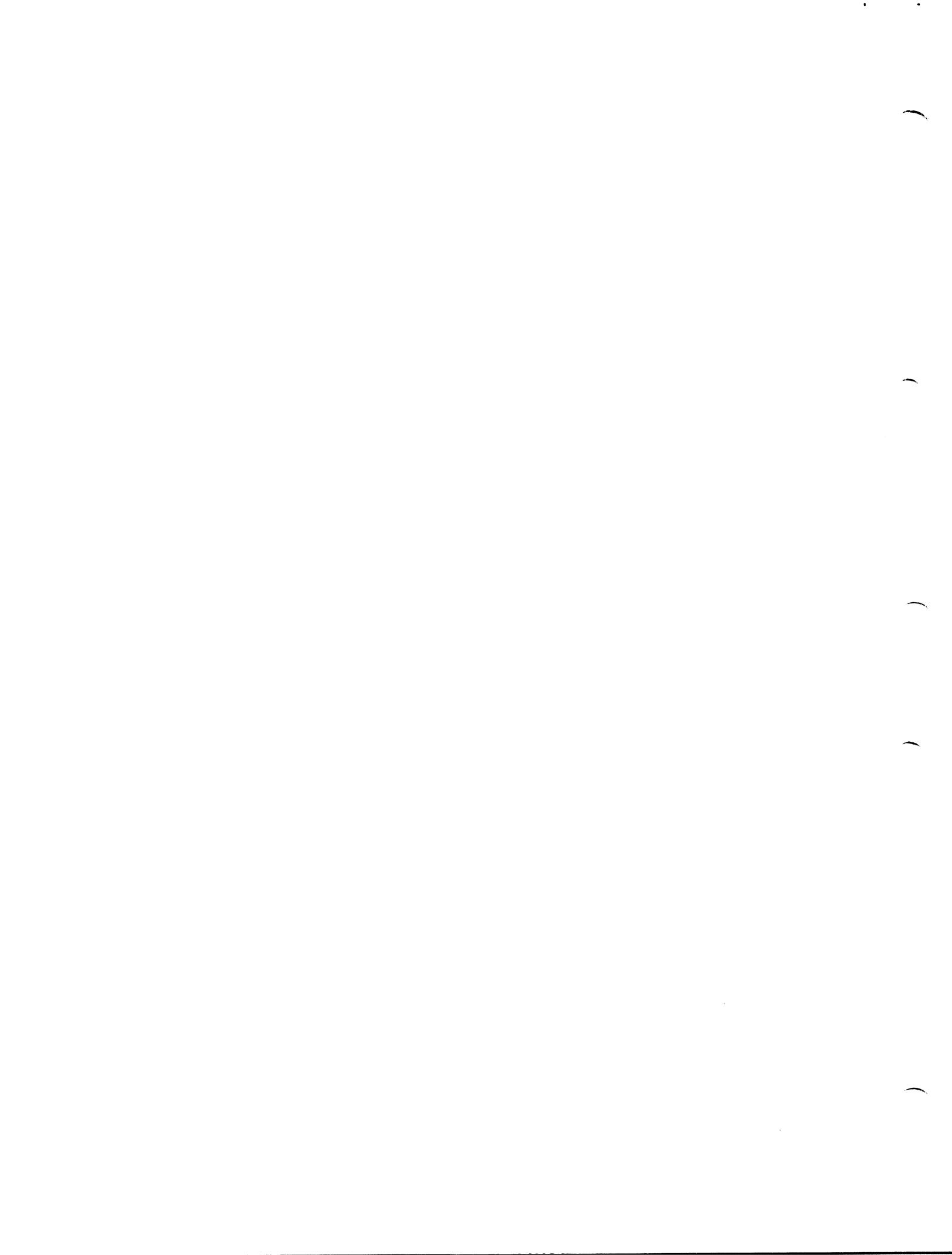
THE PROGRAM PERFORMS 4096 RANDOM TESTS IN APPROXIMATELY 5
SECONDS. THE PROGRAM WILL TYPE "T" AFTER
EACH 4096 RANDOM TESTS UNLESS SR03=1.

9. PROGRAM DESCRIPTION

THE PROGRAM IS LOADED INTO LOCATIONS 6600-7577,
WITH TEMPORARY STORAGE LOCATIONS ON PAGE 2.

THE PROGRAM USES SEPARATE RANDOM NUMBER GENERATORS TO GENERATE
THE TAD INSTRUCTION, INSTRUCTION AND DATA ADDRESSES, AND THE
TWO ARGUMENTS TO BE "TADDED". THE INSTRUCTIONS AND DATA ARE
STORED IN THEIR PREVIOUSLY GENERATED ADDRESSES. THE PROGRAM
TRANSFERS TO THE LOCATION OF THE INSTRUCTION AND EXECUTES IT.
THE PROGRAM THEN TRANSFERS TO A COMPARISON ROUTINE WHERE THE
ACTUAL RESULT OF THE TAD INSTRUCTION IS COMPARED TO A SIMULATED
TAD. THE SIMULATOR IS SIMILAR TO THE ONE USED IN MAINDEC-
BE-D0CA-D, NO TADS ARE USED IN THE PROGRAM ITSELF.

AFTER 4096 TESTS, THE PROGRAM TYPES "T" AND CONTINUES TESTING.



/RANDOM TAD TEST
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 /V 82 07552

/TEMPORARY STORAGE LOCATIONS

```
*0000 RETURN, 0
0001 INSTL, JMP
0002 INADDL, 2
0003 DATADDL, 3
0004 PADDL, 0
0005 IFLAGL, 0
7501 MQA=7501
7421 MQL=7421
6007 CAF=6007
*2000 ADVANCE-COPY
*2001 START, CAF
      JMP 1      +1
*2002 STARTL
*2003
*2004
*2005
```

document subject to change
 without notice.

/GENERATE TEST INSTRUCTION AND DATA

```
6600 *6600
7301 STARTL, CLA CLL CNTR1 /CLEAR PASS COUNTER
6601 3376 DCA CNTR1
6602 7604 TEST1L, LAS SR11 /TEST SR11
6603 0371 AND SR11 /IS SR11=1
6604 7640 SZA CLA /SR11=1, DO NOT GENERATE INSTRUCTION
6605 5224 JMP TDAT1L /GENERATE INSTRUCTION
6606 4746 JMS 1 TGENL /SAVE INDIRECT FLA
6607 3355 DCA TIFGL
6610 7040 CMA INSTL /GET INSTRUCTION
6611 0001 AND INSTL /SAVE IT
6612 3353 DCA TINSTL
6613 7041 CMA AND INADDL /GET INSTRUCTION ADDRESS
6614 0002 DCA TINADDL /SAVE IT
6615 3354 CMA AND DATADL /GET DATA ADDRESS
6616 7040 CMA AND DATADL
6617 0003 DCA TDATDL /SAVE IT
6620 3356 CMA AND PADDL /GET INDIRECT TO DATA
6621 7041 CMA AND TPADDL
6622 0004 DCA LAS SR10 /TEST SR10
6623 3357
6624 7604 TDAT1L
6625 0372 AND
```

PAL10	V141	11-NOV-70	21:32	PAGE 1-1
/		SZA CLA		/IS SR10=1, /SR10=1, DO NOT GENERATE DATA1
6626	7647	JMP TDAT2L		/GENERATE RANDOM NUMBER
6627	5234	CMA AND JMS I	TDAT1L TRANDL	
6630	7040	DCA TDAT1L		
6631	0365	LAS SR09		/TEST SR09
6632	4752	AND SZA CLA	JMP SETTL	/IS SR09=1, /SR09=1, DO NOT GENERATE DATA2
6633	3360	CMA AND JMS I	TDAT2L TRANDL	/GENERATE RANDOM NUMBER
6634	7604	DCA TDAT2L		
6635	0373	AND SZA CLA	JMP SETTL	
6636	7642	CMA AND JMS I	TDAT2L TRANDL	
6637	5244	DCA TDAT2L		
6640	7046	LAS SR09		/TEST SR09
6641	0361	AND SZA CLA	JMP SETTL	/IS SR09=1, /SR09=1, DO NOT GENERATE DATA2
6642	4752	CMA AND JMS I	TDAT2L TRANDL	/GENERATE RANDOM NUMBER
6643	3361	DCA TDAT2L		
<pre> /SET UP INSTRUCTION AND DATA AT TEST ADDRESS /ALONG WITH RETURN TO THIS ROUTINE / SETTL, CLA CLL CMA AND TINSTL 6644 7340 /GET INSTRUCTION 6645 0353 /STORE IN TEST LOCATION 6646 3754 DCA I TINADL 6647 7040 CMA AND TIFLGL 6650 0355 /GET INDIRECT FLAG 6651 7650 SNA CLA DIRL 6652 5302 /IS INSTRUCTION INDIRECT 6653 7040 /NO, GET DATA 6654 0356 CMA AND TDAT1L 6655 0367 AND T7760 6656 7640 SZA CLA NOTAUT 6657 5276 /ADDRESS IS INDIRECT 6660 7040 /IS ADDRESS AUTO-INDEX REGISTER 6661 0356 AND TDAT1L 6662 0375 AND K10 6663 7652 SNA CLA NOTAUT 6664 5276 /NO 6665 7040 JMP CMA TPADDL 6666 0357 AND CIA /ADDRESS IS AUTO-INDEX REGISTER 6667 7041 CIA AND TDAT1L /STORE IN TEST LOCATION 6670 7042 CMA I TDAT1L 6671 3756 /DECREMENT POINTER TO DATA 6672 7040 CMA AND TDAT1L 6673 0362 CIA AND TPADDL 6674 3757 CIA I DOTSTL 6675 5305 JMP TDAT1L 6676 7040 NOTAUT, CMA AND TPADDL 6677 0357 CIA I TDAT1L 6700 3756 DCA I TDAT1L 6701 5272 JMP .7 6702 7040 CMA AND TDAT1L 6703 0363 CIA I TDAT1L 6704 3756 /GET DATA /STORE IN TEST LOCATION)))))</pre>				

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) /SIMULATE "TAD"

6705 7340 DOTSTL, CLA CLL CMA
6706 0360 AND TDA1L, /GET DATA1
6707 7421 MQL /SAVE IN MQ
6710 7040 CMA /GET DATA2
6711 0361 AND /DO SIMULATION
6712 4751 JMS I TSIMAD /SAVE ANSWER
6713 3363 DCA TSIMAC
6714 7010 RAR /SAVE LINK
6715 3362 DCA TSIML

/ /GO TO TEST

6716 7040 DOANDL, CMA /GET RETURN ADDRESS
6717 0347 AND /SAVE
6720 3000 DCA
6721 7040 CMA /GET INSTRUCTION ADDRESS
6722 0354 AND /INCREMENT
6723 7001 IAC /IS IT 0
6724 7450 SNA /YES, GENERATE NEW INFORMATION
6725 5202 JMP TEST1L /NO, SAVE
6726 3345 DCA TEMP2L
6727 7040 CMA /GET RETURN INSTRUCTION
6730 0366 AND /PUT IN TEST LOCATION
6731 3745 DCA I TEMP2L
6732 7140 CLL CMA /GET DATA2
6733 0361 AND TDA2L /EXECUTE "TAD"
6734 5754 JMP I TINADL
/ /RETURN HERE AFTER EXECUTION
6735 3364 TRETUL, DCA /SAVE AC
6736 7010 RAR
6737 3365 DCA /SAVE LINK
6740 4774 JMS I /COMPARE REAL AND SIMULATED ADDITIONS
6741 2376 ISZ CNTR1
6742 5202 JMP TEST1L
6743 4750 JMS I TEPA1L
6744 5202 JMP TEST1L
/ /
6745 0000 TEMP2L, 0
6746 7000 TGENL, GENL
6747 6735 TRETTL, TRETUL
6750 7442 TEPASL, EPASL
6751 7200 TSIMAD, RSIMAD
6752 7430 TRANSL, RANDL
6753 0000 TINSTL, 0

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6754 0000 TINADL, 0
6755 0001 TIFLGL, 0
6756 0000 TDATAL, 0
6757 0000 TPADDL, 0
6760 0021 TDAL1, 21
6761 0037 TDAL2L, 37
6762 0000 TSIML, 0
6763 0000 TSIMAC, 0
6764 0000 TAC, 0
6765 0000 TLINK, 0
6766 5400 T5400L, 5400
6767 7760 T7760, 7760
6770 7770 T7770, 7770
6771 0001 SR11, 1
6772 0002 SR10, 2
6773 0004 SR09, 4
6774 7313 TCOMAD, COMAD
6775 0010 K10, 10
6776 0003 CNTR1, 0

/GENERATE INSTRUCTIONS AND ADDRESSES

7000 7000 PAGE GENL, 0
7000 0000 PAGE GENL, 0

/GENERATE "AND" INSTRUCTION

7001 7040 GANDL, CMA
7002 0350 AND R1L
7003 4762 JMS I SRANDL
7004 3350 DCA R1L
7005 7040 CMA /GENERATE RANDOM NUMBER
7006 0350 AND /SAVE NUMBER
7007 7421 MQL
7010 7040 CMA /GENERATE OP CODE
7011 0365 AND K1000
7012 7501 MQA
7013 0352 AND K1777
7014 3001 DCA INSTL
7015 7040 CMA /SAVE INSTRUCTION
7016 0001 AND K0177L
7017 0355 AND TEMP3L
7020 3361 DCA /GET INSTRUCTION
/EXTRACT PAGE ADDRESS OF INSTRUCTION
/SAVE PAGE ADDRESS OF INSTRUCTION

/GENERATE ADDRESS FOR INSTRUCTION

7021 7040 GANADL, CMA
7022 0353 AND R2L
7023 4762 JMS I SRANDL
7024 3353 DCA R2L
7025 7040 CMA /GENERATE RANDOM NUMBER
7026 0353 AND R2L
7027 4777 JMS /SAVE NUMBER
/IS ADDRESS WITHIN LIMITS

	PAL10	V1441	11-NOV-70	21:32	PAGE 1-4
7030	5221	JMP CMA AND SZA CLA	GANADL R2L P0L	/NO, GENERATE NEW ADDRESS /IS ADDRESS ON PAGE 0 /NO	
7031	7041				
7032	0353				
7033	0354				
7034	7641				
7035	5244				
7036	7042				
7037	7353				
7040	4776	PAGAL,	R2L JMS SIMA CLA	/GET PAGE ADDRESS OF INSTRUCTION /GET DIFFERENCE BETWEEN PAGE ADDRESSES /IS DIFFERENCE >2 /NO	
7041	7704				
7042	5221				
7043	5255				
7044	7041	PAGADL, CMA AND SNA CLA	INSTL K200L P0L	/GET INSTRUCTION /IS PAGE BIT SET /NO, USE ADDRESS AS IS	
7045	0001				
7046	0357				
7047	7652				
7050	5255				
7051	7041				
7052	0353				
7053	0355				
7054	5241				
7055	7041	PAL,	R2L JMP CMA AND TEMP3L SNA CLA	/PAGE BIT SET, EXTRACT PAGE ADDRESS FOR INSTRUCTION /TEST FOR INTERFERENCE /MAKE SURE DATA WILL NOT BE STORED IN LOCATION 0 /LOCATION ZERO, TRY AGAIN /USE ADDRESS AS IS	
7056	0361				
7057	7652				
7060	5201				
7061	7041				
7062	0353				
7063	3002				
7064	7041	DAADL, CMA AND SNA CLA	INSTL K200L P0AL	/GET INSTRUCTION /IS PAGE BIT OF INSTRUCTION SET	
7065	0001				
7066	0357				
7067	7652				
7070	5307				
7071	7041				
7072	0002				
7073	0352				
7074	7421				
7075	7041				
7076	0361				
7077	7501				
7100	3003	INDIRL,	MQA DCA CMA AND TEMP3L	/"OR" TOGETHER TO GET /DATA ADDRESS	
7101	7041				
7102	0001				
7103	0356				
7104	7641				
7105	5313				

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/				
7106 5600	JMP I	GENL		/EXIT
7107 7040	CMA			/USE PAGE ADDRESS OF INSTRUCTION
7110 2361	AND	TEMP3L		/AS DTAT ADDRESS
7111 3003	DCA	DATA1L		
7112 5301	JMP	INDIRL		
	/GENERATE INDIRECT ADDRESS FOR DATA			
7113 7040	CMA	R3L		/GENERATE RANDOM NUMBER
7114 0360	AND	SRANDL		
7115 4762	JMS I	R3L		
7116 3360	DCA			
7117 7040	CMA			
7120 0360	AND	R3L		
7121 4777	JMS	LIMIT		/IS ADDRESS WITHIN LIMITS
7122 5313	JMP	PADL		/NO, TRY AGAIN
7123 7040	CMA			
7124 0002	AND	INADDL		
7125 4775	JMS	ABSL1		/GET INSTRUCTION ADDRESS
7126 7700	SMA	CLA		/GENERATE DIFFERENCE BETWEEN ADDRESSES
7127 5313	JMP	PADL		/DO INSTRUCTION AND ADDRESS INTERFERE
7130 7040	CMA			/YES
7131 0003	AND	DATA1L		/NO, TEST DATA ADDRESS AS ABOVE
7132 .775	JMS	ABSL1		
7133 7700	SMA	CLA		
7134 5313	JMP	PADL		
7135 7040	CMA			
7136 0360	AND	CIA		/ADDRESSES DO NOT INTERFERE
7137 7041	CIA			/WILL LOCATION BE 0 IF DECREMENTED
7140 7040	CMA			
7141 7650	SNA	CLA		
7142 5313	JMP	PADL		/YES, GENERATE NEW POINTER
7143 7040	CMA	R3L		
7144 0360	AND	PADDL		
7145 3004	DCA			
7146 7040	CMA			
7147 5600	JMP I	GENL		
7150 0001	R1L	1		
7151 0003	K3L	3		
7152 1777	K177	1777		
7153 0005	R2L	5		
7154 7600	P0L	7600		
7155 0177	K0177L	177		
7156 0400	K400L	400		
7157 0200	K200L	200		
7158 0015	R3L	15		
7161 0002	TEMP3L	0		
7162 7430	SRANDL	RANDL		
7163 7201	SRIMAD	RSIMAD		

```

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        LIML,    1201
        K1000,   1000

        /SIMULATED ADDITION

7175    7507
7176    7474
7177    7303
        PAGE
        RSIMAD, 0      DCA      ARG2      /SAVE ARGUMENTS
7200    0000
7201    3344
7202    7501
7203    3343
        /SIMULATE ADDITION BY SIMULATED GENERATION OF SUM
        /AND CARRY BITS
        /FORM OR OF ARG1 WITH ARG2
        /SIMAD,  CLA      CLL      CMA      /LOAD AC WITH ARG1
        AND     ARG1      MQL      /PLACE IN MQ
        CMA      AND     ARG2      /LOAD AC WITH ARG2
        AND     MQA      /FORM ARG1 OR ARG2
        DCA      A10RA2    /SAVE ARG1 OR ARG2
        /FORM XOR (EXCLUSIVE OR) OF ARG1 WITH ARG2
        /BY A XOR B=(A AND NOTB)OR(NOTA AND B)
        MQA      /GET ARG1 FROM MQ
        GMA      /FORM NOTARG1
        AND     ARG2      /AND WITH ARG2 TO GET ARG2 AND NOTARG1
        MQL      /SAVE IN MQ
        CMA      AND     ARG2      /LOAD AC WITH ARG2
        AND     CMA      /FORM NOTARG2
        CMA      AND     ARG1      /AND WITH ARG1 TO GET ARG1 AND NOTARG2
        AND     MQA      /OR WITH ARG2 AND NOTARG1
        DCA      SIMAC    /TO GET ARG1 XOR ARG2
        DCA      SIMLNK

7213    7501
7214    7040
7215    0344
7216    7421
7217    7040
7220    0344
7221    7040
7222    0343
7223    7501
7224    3346
7225    3347
        /AND ARG1 WITH ARG2
        /TEST FOR CARRIES
        /IF THERE ARE NO BITS IN COMMON BETWEEN ARG1 AND ARG2
        /THERE WILL BE NO CARRIES GENERATED

```

```

/
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7226    7041      CMA          AND ARG1
7227    0343      AND ARG2
7230    0344      SNA
7231    7450      ENDSIM
7232    5274      JMP

/
/GENERATE CARRIES

/
MQL      MQA      MQL      AND A10RA2
NXTCAR, SNA      ENCAR
JMP      CLL      RAL
MQA      MQL      MQA      NXTCAR
JMP

/
/TEST FOR CARRY INTO LINK

/
MQA      AND A10RA2
AND K4 00
SNA      ENCAR1
JMP      DCA      SIMLNK
DCA      XORALL
JMP      CML      RAR
CLL      AND ARG1
CML      AND ARG2
SZA      SIMLNK
DCA

/
/FORM XOR OF ARG1, ARG2, AND CARRIES
/TO GET FINAL SIMULATED SUM

/
MQA      DCIA      CARRY
AND MQA
CMA      AND MQL
CML      AND CMA
AND SIMAC
CMA      SIMAC
CML      SIMAC
AND CARRY
CMA      CARRY
MQA      SIMAC
CML      SIMAC
CML      SIMAC
AND CML
SZA      CLA
CLA      SIMLNK
CML      CLA
SZA      CLA
CML
ENDSIM.

7233    7421      MQL      MQA      MQL      AND A10RA2
7234    7521      NXTCAR, SNA      ENCAR
7235    0345      JMP      CLL      RAL
7236    7456      MQA      MQL      MQA      NXTCAR
7237    5244      JMP
7240    7104      MQA      MQL      MQA      NXTCAR
7241    7521      JMP
7242    7501      MQA      MQL      MQA      NXTCAR
7243    5234      JMP

/
/SAVE FIRST CARRIES
/GET CARRIES FROM MQ
/AND WITH A10RA2 TO SEE IF MORE CARRIES ARE GENERATED
/ARE THERE ANY MORE CARRIES
/NO, END SIMULATION OF CARRIES
/PROPAGATE CARRIES
/GET PREVIOUS CARRIES FROM MQ, SAVE NEW CARRIES
/OR NEW CARRIES WITH PREVIOUS CARRIES
/CONTINUE

/
/TEST BIT 00
/IS BIT 00 1
/NO, CARRIES DID NOT PROPAGATE INTO LINK
/YES, SAVE CARRY INTO LINK
/COMPLETE SIMULATION
/SET AC=4000
/AND WITH ARG1
/AND WITH ARG2 TO SEE IF ORIGINAL
/NUMBERS GENERATED CARRY INTO LINK
/SAVE SIMULATED LINK

/
/FORM CARRY AND NOTA10RA2
/OR WITH CONTENTS OF MQ

```

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```

CMA
AND
JMP I
SIMAC
RSIMAD
/TO GET FINAL SIMULATED SUM

/TEST ADDRESS
/COMPARE SIMULATED AND REAL RESULT
/COMAD, 0 CLL CMA
AND SZA
SEA CLA
CML
GMA
AND SZA
CLA
TLINK
/GET SIMULATED RESULTANT LINK
/COMPARE TO REAL LINK
/SZL
JMP ERROR1
CLA CLL
CMA AND TAC
CMA AND TSIMAC
/IF SAME, LINK=0
/NOT THE SAME, ERROR
/GET ADDITION RESULT
/COMPARE TO COMPLEMENT OF SIMULATION RESULT
/SZA
JMP ERROR1
CMA AND TSIMAC
CMA AND TAC
SZA CLA
/NOT 0, ERROR
/GET SIMULATION RESULT
/COMPARE TO COMPLEMENT OF REAL ADDITION
/ERR1, JMS I
ERRORS
JMP I
COMAD
ARG1,
0
ARG2, 0
A1ORA2, 0
SIMAC, 0
SIMLNK, 0
K4000, 4000
CARRY, 0
ERRORS, ERROR
7440
73333 5341
7334 7342
7335 0773
7336 7040
7337 0774
7340 7640
7341 4752
7342 5713
7343 0000
7344 0020
7345 0000
7346 0000
7347 0000
7350 4000
7351 0000
7352 7400

```

```

/          /ERROR HANDLER
/
```

```

7373 6763
7374 6764
7375 6765
7376 6762
7377 7164      PAGE
7400 0000      ERROR, 0
7401 7604      LAS
7402 0267      AND SR00
7403 7640      SZA CLA
7404 5600      JMP I  ERROR
7405 7240      CLA CMA
7406 0777      AND TDAL
7407 7402      HLT
7410 7240      CLA CMA
7411 0776      AND TDA2L
7412 7402      HLT
7413 7240      CLA CMA
7414 0775      AND TINSTL
7415 7402      HLT
7416 7240      CLA CMA
7417 0774      AND TINADL
7420 7402      HLT
7421 7240      CLA CMA
7422 0773      AND TDATL
7423 7402      HLT
7424 7240      CLA CMA
7425 0772      AND TPADDL
7426 7402      HLT
7427 5600      JMP I  ERROR
7430 0000?     RANDL, 0
7431 7104      CLL RAL
7432 7420      SNL
7433 5240      JMP ENRAN
7434 7421      MQL
7435 7040      CMA
7436 0241      AND K3
7437 4771      JMS RSIMAD
7440 5630      ENRAN, K3,
7441 0003      JMP I  3
7442 0000?     EPASL, 0
7443 7604      LAS

```

)

```

/END OF PASS
/          /EPASL, 0
/          /LAS

```

)

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7444 2270 AND SR03
7445 7642 SZA CLA
7445 5642 JMP I EPASL
7447 7042 CMA AND C215
7450 0271 JMS TYPE
7451 4261 CMA
7452 7042 AND C212
7453 0272 JMS TYPE
7454 4261 CMA
7455 7042 AND C324
7456 0273 JMS TYPE
7457 4261 JMP I EPASL
7460 5642 //

7461 0000 TYPE,
7462 6046 TLS
7463 6041 TSF
7464 5263 JMP I
7465 7201 GLA
7466 5661 TYPE
//

7467 4000 SR00,
7470 0400 SR03,
7471 0215 C215,
7472 0212 C212,
7473 0324 C324,
//TEST FOR PROPER DIFFERENCE

7474 0000 ABSL,
7475 7041 CIA
7476 7421 MQL
7477 7042 CMA AND TEMP3L
7500 0770 RSIMAD
7501 4771 JMS
7502 7500 SMA
7503 7041 CIA
7504 7001 IAC
7505 7021 IAC
7506 5674 JMP I ABSL
//

7507 0000 ABSL1,
7510 7041 CIA
7511 7421 MQL
7512 7040 CMA AND R3L
7513 0767 //

/ PAL10 V141 11-NOV-70 21:32 PAGE 1-11
/ 7514 4771 JMS RSIMAD
/ 7515 7500 SMA
/ 7516 7041 CIA
/ 7517 7001 IAC
/ 7520 7001 IAC
/ 7521 5707 JMP 1 ABSL1
/ 7567 7160 \$
/ 7570 7161
/ 7571 7200
/ 7572 6757
/ 7573 6756
/ 7574 6754
/ 7575 6753
/ 7576 6761
/ 7577 6760

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PAGE 1-12

EE-NOW=1/0

PAL10

0400
0500
0600

11000
11100
11200
11300
11400
11500
11600
11700

2000
2100
2200
2300
2400
2500
2600

3000
3100
3200
3300
3400
3500
3600
3700

4000
4100
4200
4300
4400
4500
4600
4700

5000
5100
5200
5300
5400
5500
5600
5700

6000
6100
6200
6300
6400
6500
6600
6700
7000
7100
7200
7300
7400
7500
7600
7700

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A10RA2	7345	PAGAL	7040
ABSL	7474	PAL	7055
ABSL1	7527	R1L	7150
ARG1	7343	R2L	7153
ARG2	7344	R3L	7160
C212	7472	RANDL	7430
C215	7471	RETURN	0000
C324	7473	RSIMAD	7200
CAF	6207	SETTL	6644
CARRY	7351	SIMAC	7346
CNTR1	6776	SIMAD	7204
COMAD	7313	SIMLNK	7347
DAADL	7064	SR00	7467
DATADL	6003	SR03	7470
DIRL	6702	SR09	6773
DOANDL	6716	SR10	6772
DOTSTL	6705	SR11	6771
ENCAR	7244	SRANDL	7162
ENCAR1	7253	SRIMAD	7163
ENDSIM	7274	START	0200
ENRAN	7447	STARTL	6600
EPASL	7442	T5400L	6766
ERROR	7408	T7760	6767
ERROR1	7341	T7770	6770
ERRORS	7352	TAC	6764
GANADL	7021	TCOMAD	6774
GANDL	7001	TDA1L	6760
GENL	7000	TDA2L	6761
IFLAGL	0005	TDATE1L	6624
INADDL	0002	TDATE2L	6634
INDIRL	7101	TOATAL	6756
INSTL	7061	TEMP2L	6745
K0177L	7155	TEMP3L	7161
K10	6775	TEPASL	6750
K1000	7165	TERROR	6750
K1777	7152	TEST1L	6602
K2000L	7157	TGENL	6746
K3	7441	TIFLGL	6755
K3L	7151	TINADL	6754
K4000	7352	TINSTL	6753
K4000L	7156	TLINK	6765
LIMIT	7303	TPADDL	6757
LIML	7164	TRANDL	6752
MQA	7501	TREYTL	6747
MQL	7421	TRETUL	6735
NOTAUT	6676	TSIMAC	6763
NXTCAR	7234	TSIMAD	6751
PAL	7107	TSIML	6762
PUL	7154	TYPE	7461
PADDL	6904	XORALL	7260
PADDL	7113		
PAGADL	7044		

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ERRORS DETECTED: 0

LINKS GENERATED: 23

RUN-TIME: 5 SECONDS

2K CORE USED