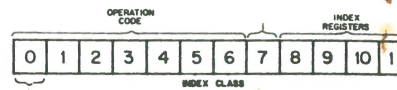
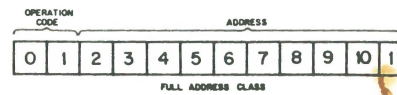
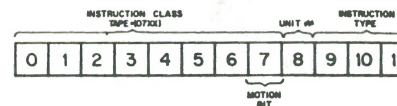


LINC INSTRUCTION FORMATS



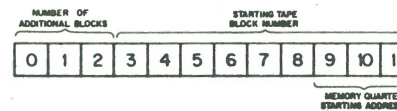
TAPE INSTRUCTIONS (2 WORDS)



FIRST WORD

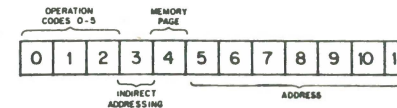


SECOND WORD (Single block class)

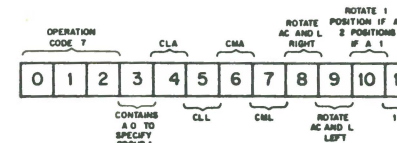


SECOND WORD (Multiple block class)

PDP-8 INSTRUCTION FORMATS



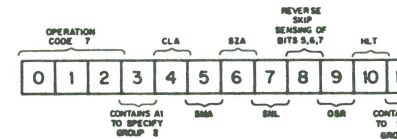
Memory Reference Instruction Bit Assignments



Logical Sequences:

- 1—CLA, CLL
- 2—CMA, CML
- 3—IAC
- 4—RAR, RAL, RTR, RTL

Group 1 Operate Instruction Bit Assignments



Logical Sequences:

- 1 (Bit 8 is Zero) — Either SMA or SZA or SNL
- 1 (Bit 8 is One) — Both SPA and SNA and SZL
- 2 — CLA
- 3 — OSR, HLT

Group 2 Operate Instruction Bit Assignments