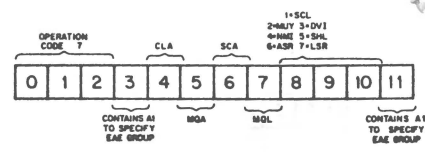


IOT Instruction Bit Assignments



- Logical Sequence:**
- 1 — CLA
 - 2 — MQA, MQL, SCA
 - 3 (Bits 8 thru 10 = 1) — SCL
 - 3 (Bits 8 thru 10 = 2) — MUY
 - 3 (Bits 8 thru 10 = 3) — DVI
 - 3 (Bits 8 thru 10 = 4) — NMI
 - 3 (Bits 8 thru 10 = 5) — SHL
 - 3 (Bits 8 thru 10 = 6) — ASR
 - 3 (Bits 8 thru 10 = 7) — LSR

EAE Microinstruction Bit Assignments

Mnemonic Code	Function	Time (μsec.)
MEMORY		
LDF	0640 set data field	1.6
LIF	0600 set instruction field	1.6
	<i>Follow by JMP≠0</i>	
LINC TAPE		
RDE	0702 read 1 tape block into memory	3.2 and pause (Actual times depend upon tape position. Pause is optional, and program may continue after 3.2 μsec. instruction time.)
RDC	0700 read 1 tape block into memory and check	
RCG	0701 read n tape block into memory and check	
WRI	0706 write 1 tape block from memory	
WRC	0704 write 1 tape block from memory and check	
WRG	0705 write n tape blocks from memory and check	
CHK	0707 check 1 tape block	
MTB	0703 move tape toward specified block	
AXO	0001 AC → extended tape operations	1.6
XOA	0021 extended tape operations buffer → AC	1.6
TAC	0003 tape accumulator buffer → AC	1.6
TMA	0023 AC → tape accumulator buffer	1.6

TRAPPED INSTRUCTIONS

501 to 515	operate 01 — 15	3.2
740 to 757	execute	3.2
540 to 557	undefined	3.2
1900	undefined	3.2
1717	undefined	3.2
700 to 717	LINC tape operation (these instructions are trapped if the trap bit is set)	3.2

† Times shown are for I=1 β=00. Other cases add 1.6 μsec.
 * Program can continue after 4.8 μsec